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Rev. 1.0

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Revision History

REV	DESCRIPTION
VO	First release

Board Change History

SCHEMATIC MN	HW VERSION	REVISION	DESCRIPTION OF CHANGE

Pillbox VOM5M8883GPIO Assignment				
Interrupt Function	Mpio Pad	Reset State	Function	MIPI Function
	GPD_0	NIB	PD	BISP1_3
Support interrupt	GPD_1	DSP_PVR_IP2_EN	PD	BISP1_2
	GPD_2	KEY_SDA	PD	BISP1_1
	GPD_3	KEY_SCL	PD	BISP1_0
	GPD_4	M5M1UARE_TX	PD	BISP8_3
Support interrupt	GPD_5	M5M1UARE_RX	PD	BISP8_2
	GPD_6	VFB	PD	BISP8_1
	GPD_7	CAM1_PWIN	PD	BISP8_0
	GPD_8	TP2512	PD	BISP4_3
Support interrupt	GPD_9	TP2514	PD	BISP4_2
	GPD_10	TP2510	PD	BISP4_1
Support interrupt	GPD_11	TP2511	PD	BISP4_0
	GPD_12	---	PD	MP_VSYNC_E
	GPD_13	CAM1_MCH0	PD	CAM1_MCH0
	GPD_14	CAM1_MCH1	PD	CAM1_MCH1
	GPD_15	CAM1_MCH2	PD	CAM1_MCH2
	GPD_16	MP_VSYNC_P	PD	CAM1_MCH3
	GPD_17	CCL_EC_SDA0	PD	CCL_EC1_SDA
	GPD_18	CCL_EC_SCL0	PD	CCL_EC1_SCL
	GPD_19	CCL_EC_SDA1	PD	CCL_EC1_SDA
	GPD_20	CCL_EC_SCL1	PD	CCL_EC1_SCL
	GPD_21	FRONT_CAM1_DATA0_E	PD	CCL_TIMER0
Support interrupt	GPD_22	FL_STROBE_TRIG	PD	CCL_TIMER1
	GPD_23	ICD_ID1	PD	BISP1_SPL_CS3_N
Support interrupt	GPD_24	FRONT_CAM1_D	PD	BISP1_SPL_CS9A_N
	GPD_25	BL_IRQB	PU	BISP2_SPL_CS1_N
Support interrupt	GPD_26	REAR_CAM1_ID	PD	CCL_ASYNC0
	GPD_27	REAR_CAM1_D	PD	BISP1_SPL_CS3A_N
	GPD_28	CAM2_PWIN	PD	BISP1_SPL_CS8_N
	GPD_29	CAM2_DATA0_EN	PD	BISP2_SPL_CS2_N
	GPD_30	CAM0_PWIN	PD	BISP2_SPL_CS3_N
	GPD_31	BISP2_DSP_MONI	PD	BISP2_3
	GPD_32	BISP2_DSP_CLK	PD	BISP2_0
	GPD_33	BISP2_DSP_CS	PD	BISP2_1
Support interrupt	GPD_34	BISP2_DSP_MBO	PD	BISP2_2
	GPD_35	---	PD	PCIE0_RST_N
Support interrupt	GPD_36	---	PD	PCIE0_CLKREQ_N
Support interrupt	GPD_37	---	PD	---
Support interrupt	GPD_38	CC_DR	PD	USB_PIC1PS
	GPD_39	DSP_PVR_IP2_EN	PD	IPASS_SIMBUS_DP
Support interrupt	GPD_40	DSP_RSTN	PD	SD_VBIE_PROHIB
	GPD_41	DSP_GPD	PD	BISP3_3
Support interrupt	GPD_42	DSP_IRQ	PD	BISP3_2
	GPD_43	MMPA_EC3_SDA	PD	BISP3_1
	GPD_44	MMPA_EC3_SCL	PD	BISP3_0
	GPD_45	BU_HCI_UARE_TXD	PD	BISP3_3
Support interrupt	GPD_46	BU_HCI_UARE_RXD	PD	BISP3_2
	GPD_47	BU_HCI_UARE_CTS_N	PD	BISP3_1
	GPD_48	BU_HCI_UARE_RRS_N	PD	BISP3_0
	GPD_49	---	PD	BISP3_3
Support interrupt	GPD_50	---	PD	BISP3_2
	GPD_51	DSP_EC3_SDA	PD	BISP9_1
	GPD_52	DSP_EC3_SCL	PD	BISP9_0
Support interrupt	GPD_53	COEBC_NIB_N	PD	BISP7_3
Support interrupt	GPD_54	COEBC_NIB_N	PD	BISP7_2
	GPD_55	APPS_EC7_SDA	PD	BISP7_1
Support interrupt	GPD_56	APPS_EC7_SCL	PD	BISP7_0
Support interrupt	GPD_57	FORCED_USB_BOOT	PD	BISP10_SPL_CS3_N

Support interrupt	GPD_62	---	PD	MS_4_DE
Support interrupt	GPD_63	TP2521	PD	MS_4_DE
Support interrupt	GPD_64	CODEC_RST_N	PD	MS_1_MCLK
	GPD_65	MAX_DSP_EN	PD	BISP10_3
Support interrupt	GPD_66	MAX_DSP_EN	PD	BISP10_2
	GPD_67	MAX_BL_CE_1	PD	BISP10_1
	GPD_68	TP2535	PD	BISP10_0
	GPD_69	SPRR_ES_CLK	PD	SPRR_ES_MCLK
	GPD_70	IPASS_SIMBUS_CLK	PD	IPASS_SIMBUS_DP
Support interrupt	GPD_71	PASS_SIMBUS_DATA	PD	IPASS_SIMBUS_DP
	GPD_72	PASS_SIMBUS_DATA	PD	IPASS_SIMBUS_DP
Support interrupt	GPD_73	BFM1_SIMBUS_DATA	PD	BFM1_SIMBUS_DATA
	GPD_74	BFM1_SIMBUS_CLK	PD	BFM1_SIMBUS_CLK
	GPD_75	MMPA_ES3_SCK	PD	MS_3_SCK
	GPD_76	MMPA_ES3_VS	PD	MS_3_VS
Support interrupt	GPD_77	MMPA_ES3_DIN	PD	MS_3_DD
Support interrupt	GPD_78	MMPA_ES3_DOUT	PD	MS_3_DI
Support interrupt	GPD_79	MMPA_ES3_MCLK	PD	MS_2_MCLK
Support interrupt	GPD_80	MMPA_RST_N	PD	MS_2_SCK
	GPD_81	BISP_SPL_MOSI2_HF	PD	BISP12_3
	GPD_82	BISP_SPL_MOSI2_HF	PD	BISP12_2
Support interrupt	GPD_83	BISP_SPL_CS_NIB2_HF	PD	BISP12_1
	GPD_84	BISP_SPL_CLK2_HF	PD	BISP12_0
	GPD_85	---	PD	BISP5_3
Support interrupt	GPD_86	---	PD	BISP5_2
	GPD_87	P_EC_SDA_BISP5_C	PD	BISP5_1
	GPD_88	P_EC_SCL_BISP5_C	PD	BISP5_0
	GPD_89	TS_RESET_N	PD	TSF1_CLK
	GPD_90	TP2537	PD	BISP1_SPL_CS1_N
Support interrupt	GPD_91	---	PD	TSF1_DATA
Support interrupt	GPD_92	---	PD	TSR2_ERROR
	GPD_93	---	PD	TSR2_CLK
	GPD_94	LC00_RESET_N	PD	TSR2_EN
Support interrupt	GPD_95	---	PD	TSR2_DATA
	GPD_96	---	PD	TSR2_SYNC
Support interrupt	GPD_97	RHEB3_CLK	PD	RHEB3_CLK
	GPD_98	RHEB3_DATA	PD	RHEB3_DATA
	GPD_99	---	PD	GRC_10
	GPD_100	---	PD	GRC_32
Support interrupt	GPD_101	GRC4	PD	GRC_4
	GPD_102	GRC5	PD	GRC_5
	GPD_103	GRC6	PD	GRC_6
Support interrupt	GPD_104	---	PD	GRC_7
	GPD_105	UM2_DATA	PD	UM2_DATA
Support interrupt	GPD_106	UM2_CLK	PD	UM2_CLK
	GPD_107	UM2_RESET	PD	UM2_RESET
Support interrupt	GPD_108	UM2_PRESENT	PD	UM2_PRESENT
	GPD_109	UM1_DATA	PD	UM1_DATA
Support interrupt	GPD_110	UM1_CLK	PD	UM1_CLK
	GPD_111	UM1_RESET	PD	UM1_RESET
Support interrupt	GPD_112	UM1_PRESENT	PD	UM1_PRESENT
Support interrupt	GPD_113	UM1_BATT_ALARM	PD	UM1_BATT_ALARM
PVRED1_BUA	GPD_113	PULL_DOWN	UM1_BATT_ALARM	Function

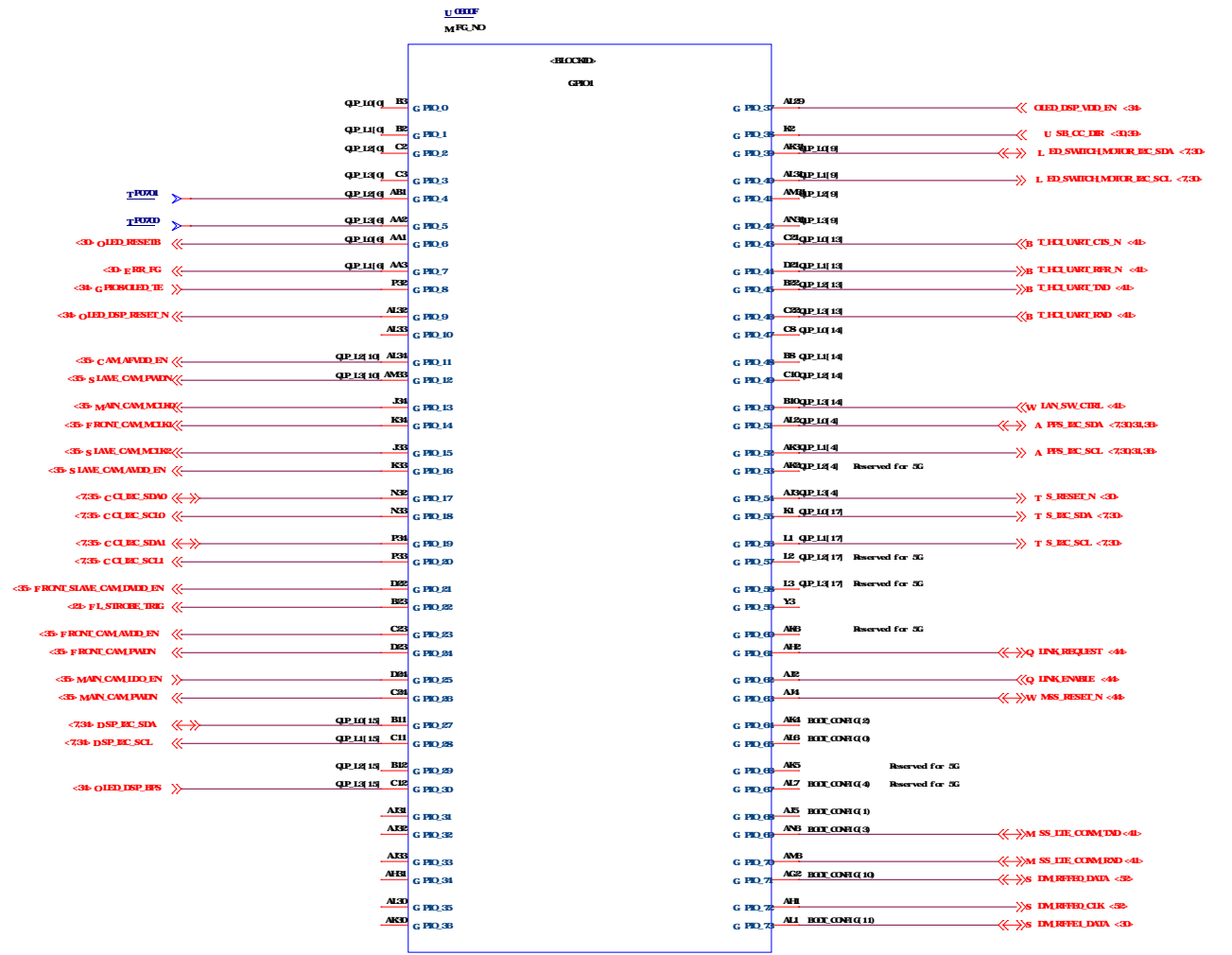
Support int	GPD_114	GRC8	PD	GRC_8
Support int	GPD_115	GRC9	PD	GRC_9
Support int	GPD_116	---	PD	FORCE_M5A_AUR1
Support int	GPD_117	ACCE1_NF	PD	QSS_TRDATA_0A
Support int	GPD_118	GRC10	PD	QSS_TRDATA_1A
Support int	GPD_119	MIG_NUN	PD	QSS_TRDATA_2A
Support int	GPD_120	ASFG_NUN	PD	QSS_TRDATA_3A
Support int	GPD_121	FP_NUN	PD	QSS_TRDATA_4A
Support int	GPD_122	KEY_NF	PD	QSS_TRDATA_5A
Support int	GPD_123	SEL_SW_SEL	PD	QSS_TRCLK_A
Support int	GPD_124	HNL_NUN	PD	QSS_TRCLK_A
Support int	GPD_125	TS_NUN	PD	---
Support int	GPD_126	---	PD	---
Support int	GPD_127	GRC3	PD	GRC_3
	GPD_128	FP_RST_N	PD	---
Support int	GPD_129	---	PD	PAS1_BOOT_3
	GPD_130	QINK_REQUEST	PD	QINK_REQUEST
Support int	GPD_131	QINK_ENABLE	PD	QINK_ENABLE
Support int	GPD_132	GRC2	PD	GRC_2
Support int	GPD_133	---	PD	---
	GPD_134	VMS_RESET_N	PD	---
	GPD_135	PA_NDCAOR_CR	PD	GRC_0
	GPD_136	VO_MMV_GRC1	PD	GRC_1
	GPD_137	RHE3_DATA	PD	RHE3_DATA
	GPD_138	RHE3_CLK	PD	RHE3_CLK
	GPD_139	RHE4_DATA	PD	RHE4_DATA
	GPD_140	RHE4_CLK	PD	RHE4_CLK
	GPD_141	RHE5_DATA	PD	RHE5_DATA
	GPD_142	RHE5_CLK	PD	RHE5_CLK
	GPD_143	GNS_EN	PD	---
	GPD_144	MS_DE_COMMAND	PD	DE_COEX_TX
?	GPD_145	MS_DE_COMMAND	PD	DE_COEX_RX
	GPD_146	RHE2_DATA	PD	RHE2_DATA
	GPD_147	RHE2_CLK	PD	RHE2_CLK
	GPD_148	RHE1_DATA	PD	ALL_USE_SIGNAL_N
	GPD_149	RHE1_CLK	PD	---

SSC Que				
Interrupt Function	Mpio Pad	Signal Name	Reset State	Function
	SSC_0	---	PD	SSC_SPL1_CS1_N
	SSC_1	SSC_PVR_EN	PD	SSC_PVR_EN
	SSC_2	SSC_EC_3_SDA	PD	SSC_EC_3_SDA
	SSC_3	SSC_EC_3_SCL	PD	SSC_EC_3_SCL
	SSC_4	TP2582	PD	SSC_EC_2_SDA
	SSC_5	TP2583	PD	SSC_EC_2_SCL
	SSC_6	TP2584	PD	SSC_UARE_3_TX
	SSC_7	TP2585	PD	SSC_UARE_3_RX
	SSC_8	TP2586	PD	SSC_SPL1_CS_N
	SSC_9	TP2589	PD	SSC_SPL1_CLK
	SSC_10	TP2585	PD	SSC_SPL1_MONI
	SSC_11	TP2586	PD	SSC_SPL1_MBO
	SSC_12	SSC_UARE_1_TX	PD	SSC_UARE_1_TX
	SSC_13	SSC_UARE_1_RX	PD	SSC_UARE_1_RX
	SSC_14	SSC_DEBLG_UARE_TX	PD	SSC_UARE_2_TX
	SSC_15	SSC_DEBLG_UARE_RX	PD	SSC_UARE_2_RX
	SSC_16	---	PD	SSC_SPL3_CLK

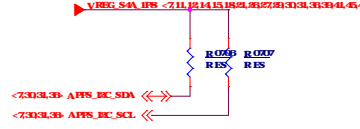
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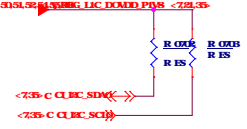




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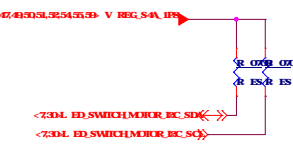
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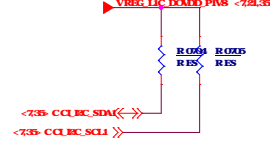
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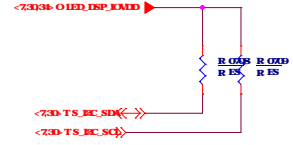
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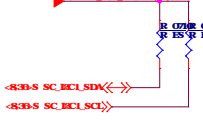
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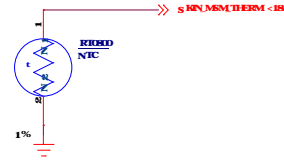
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Sensor



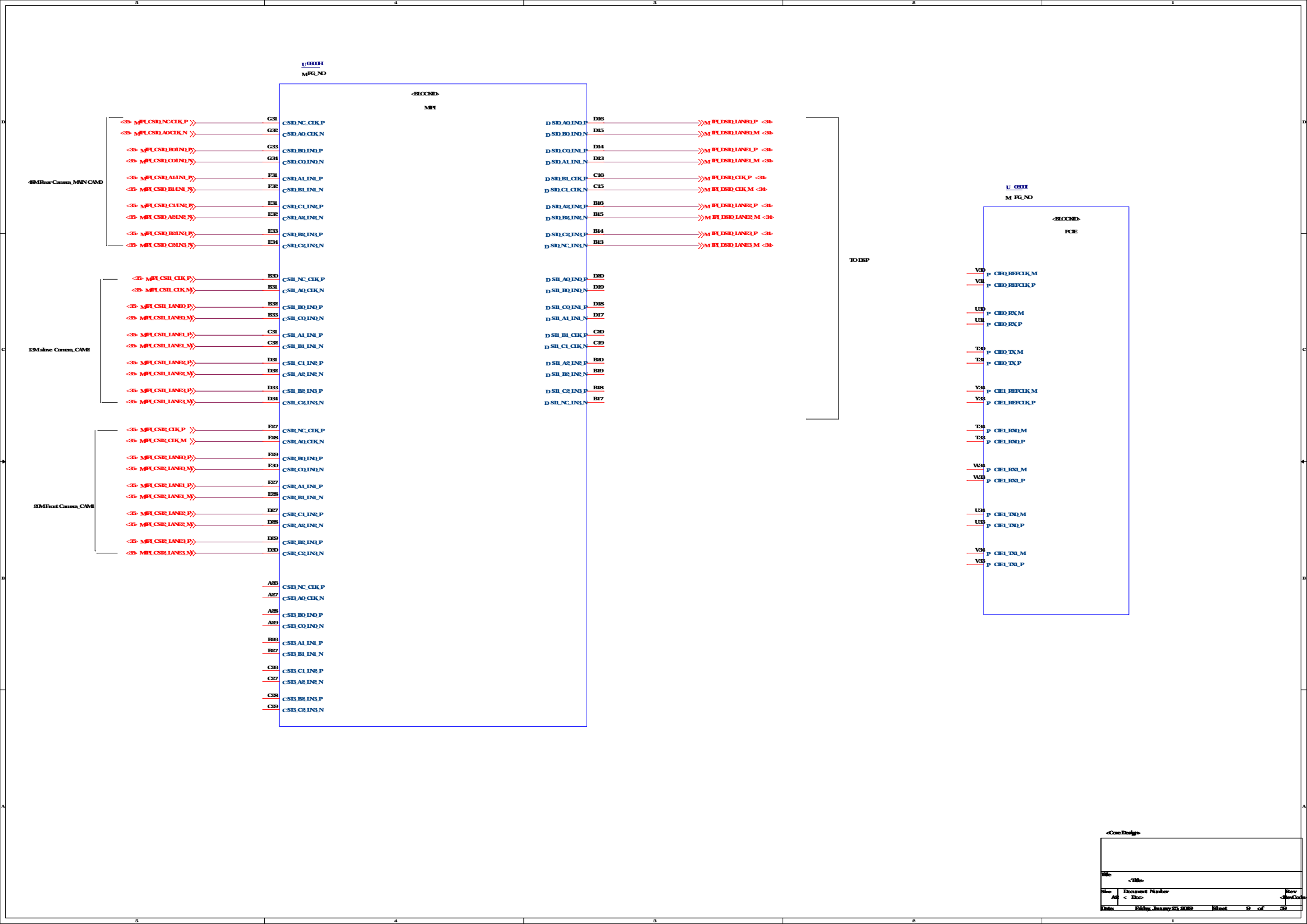
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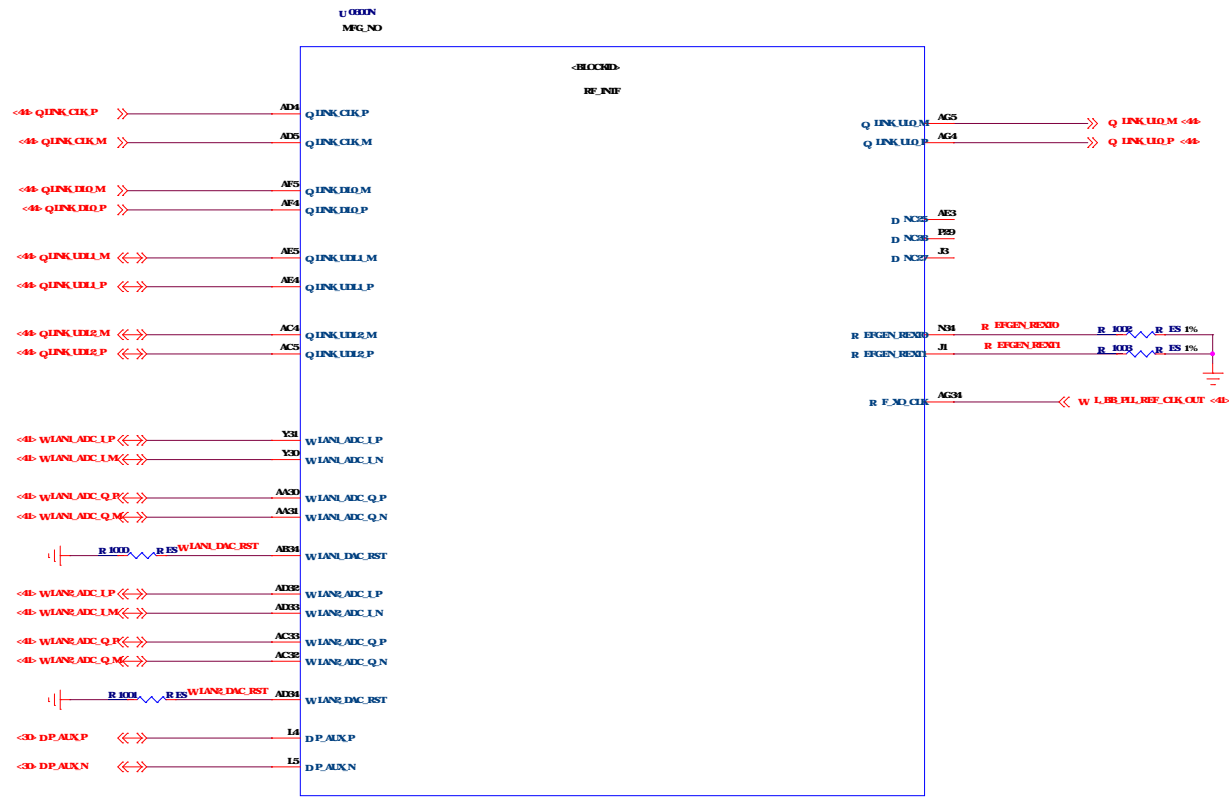


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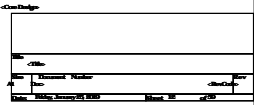
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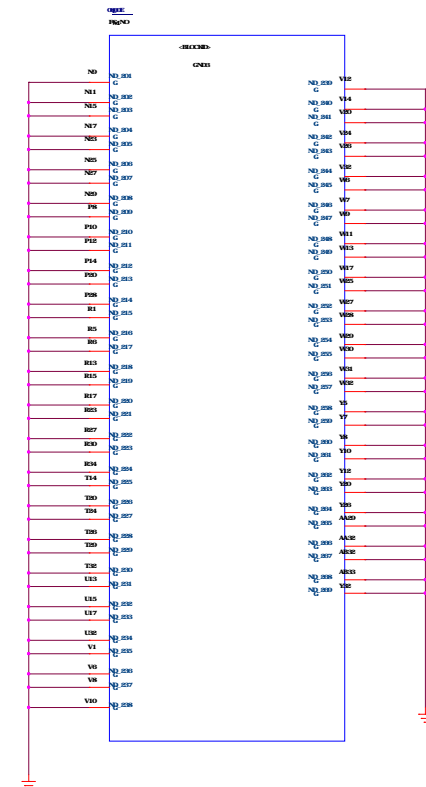
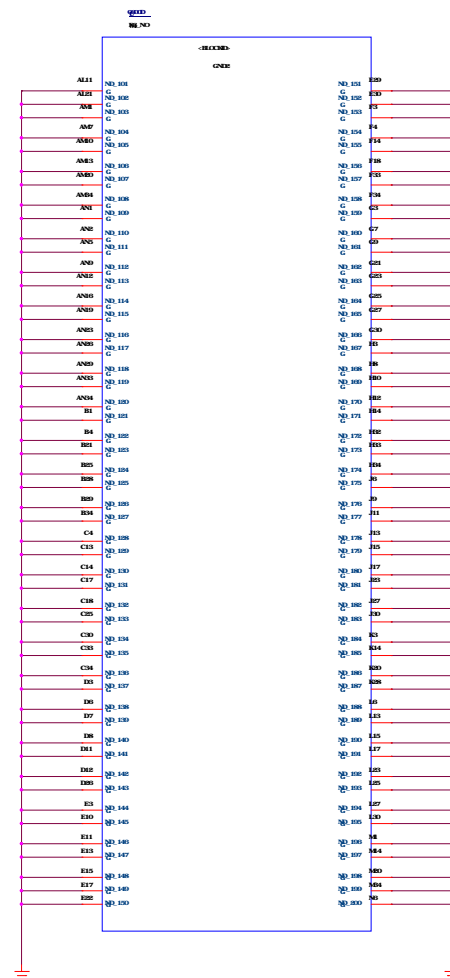
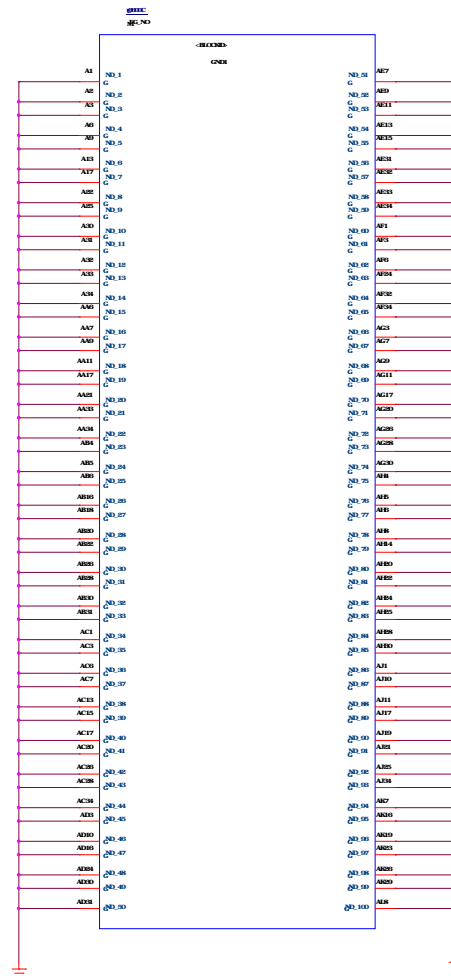




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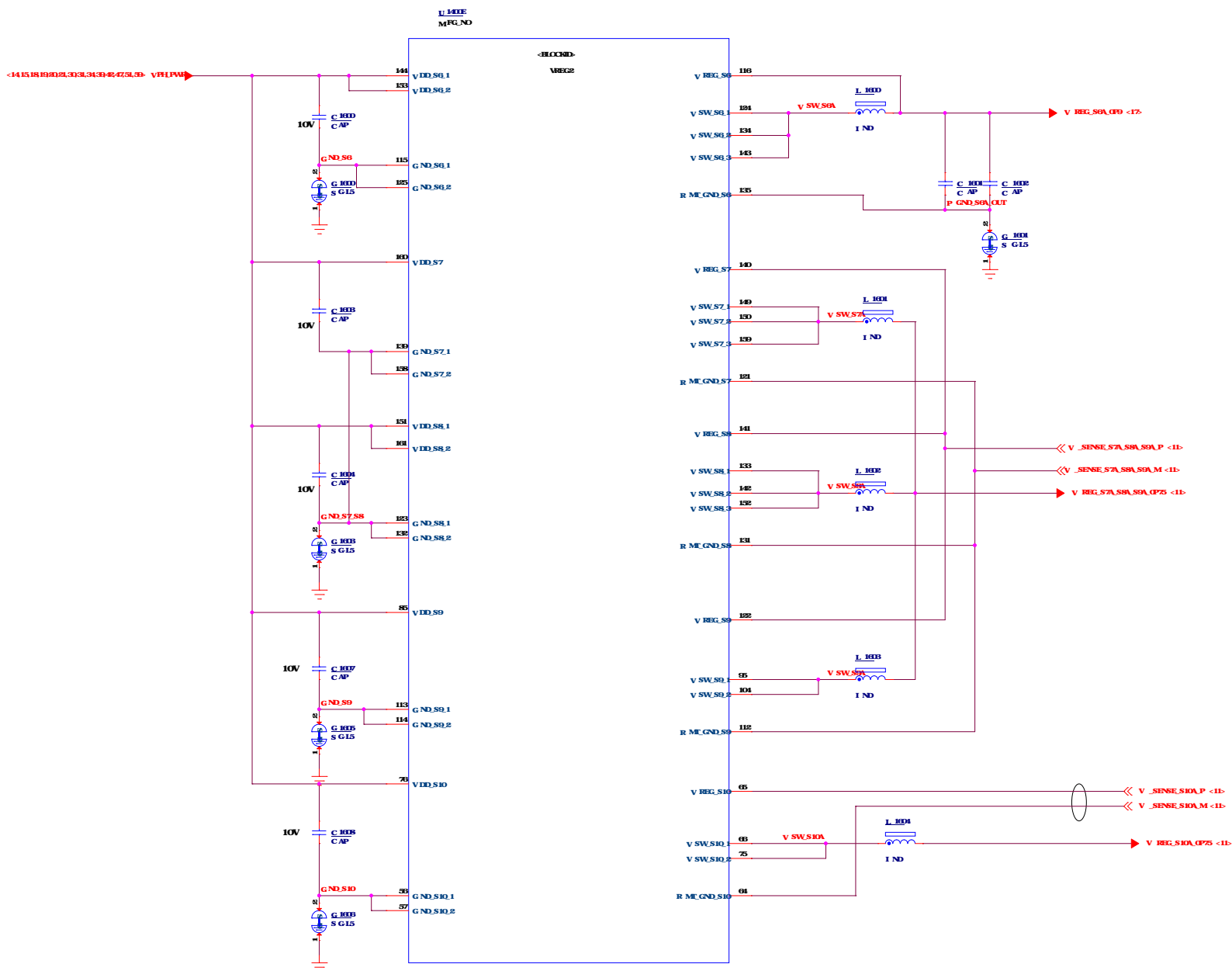






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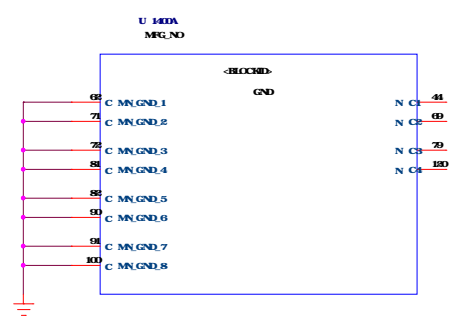
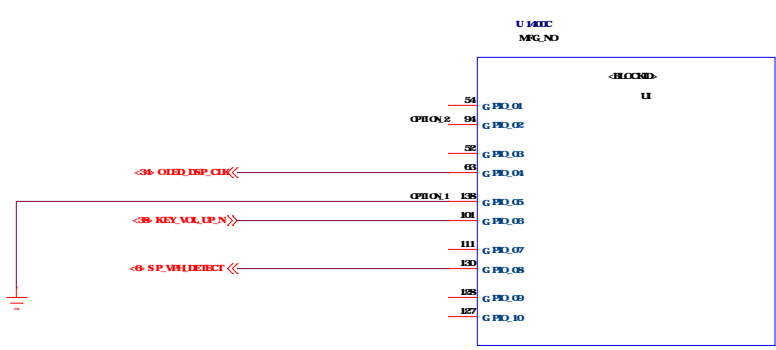
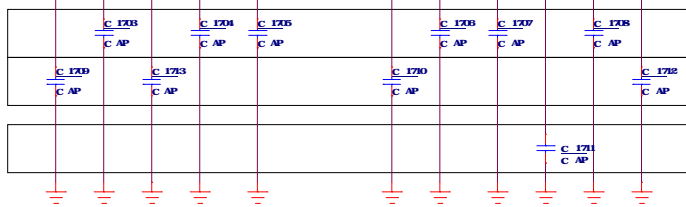
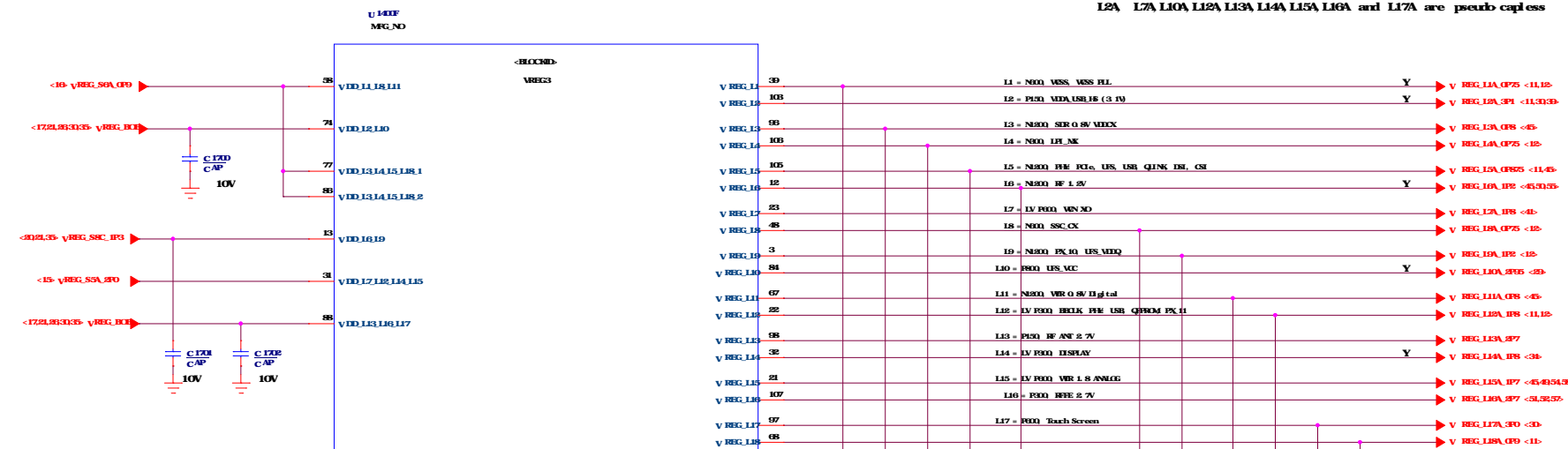
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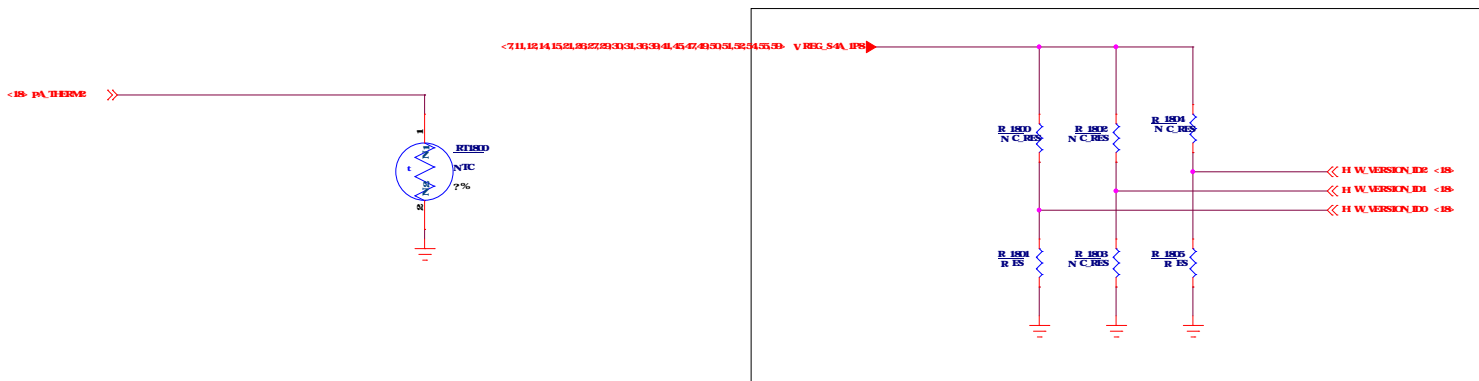
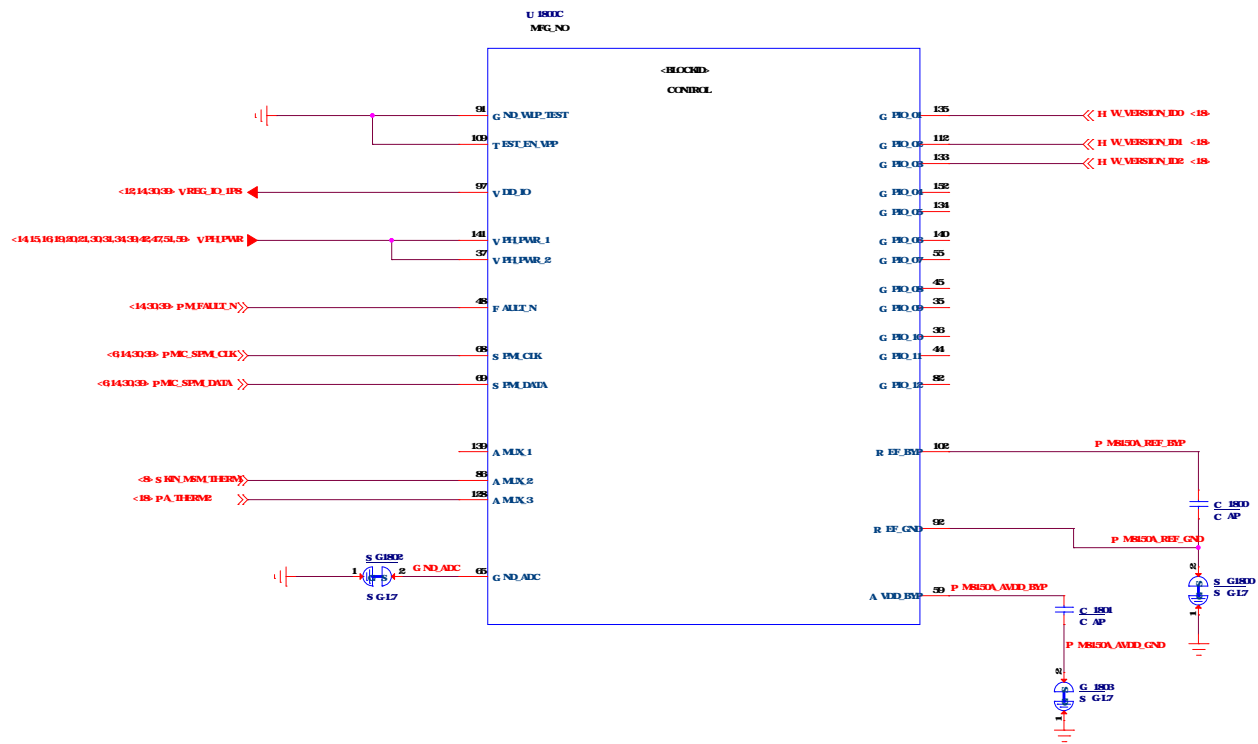
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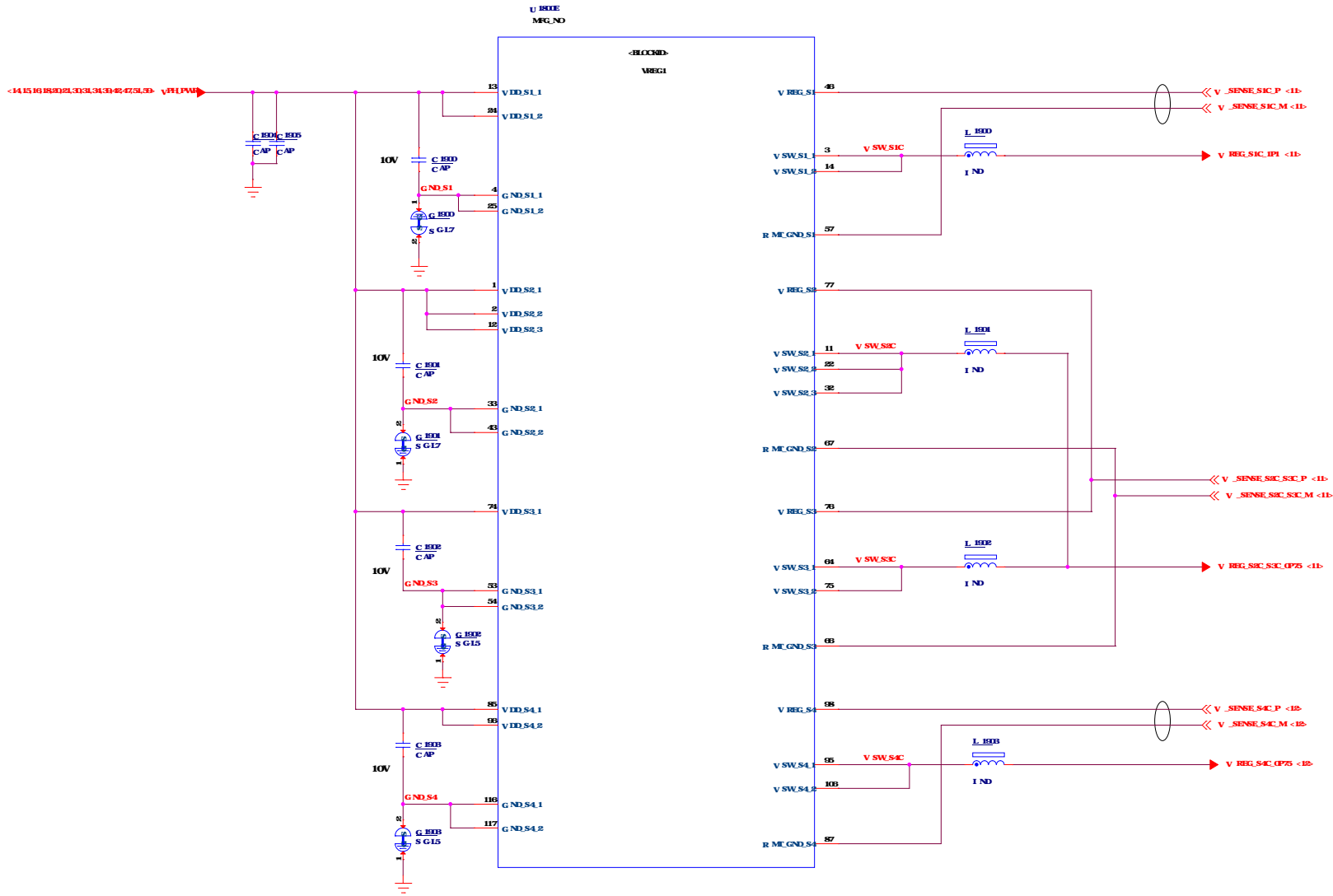
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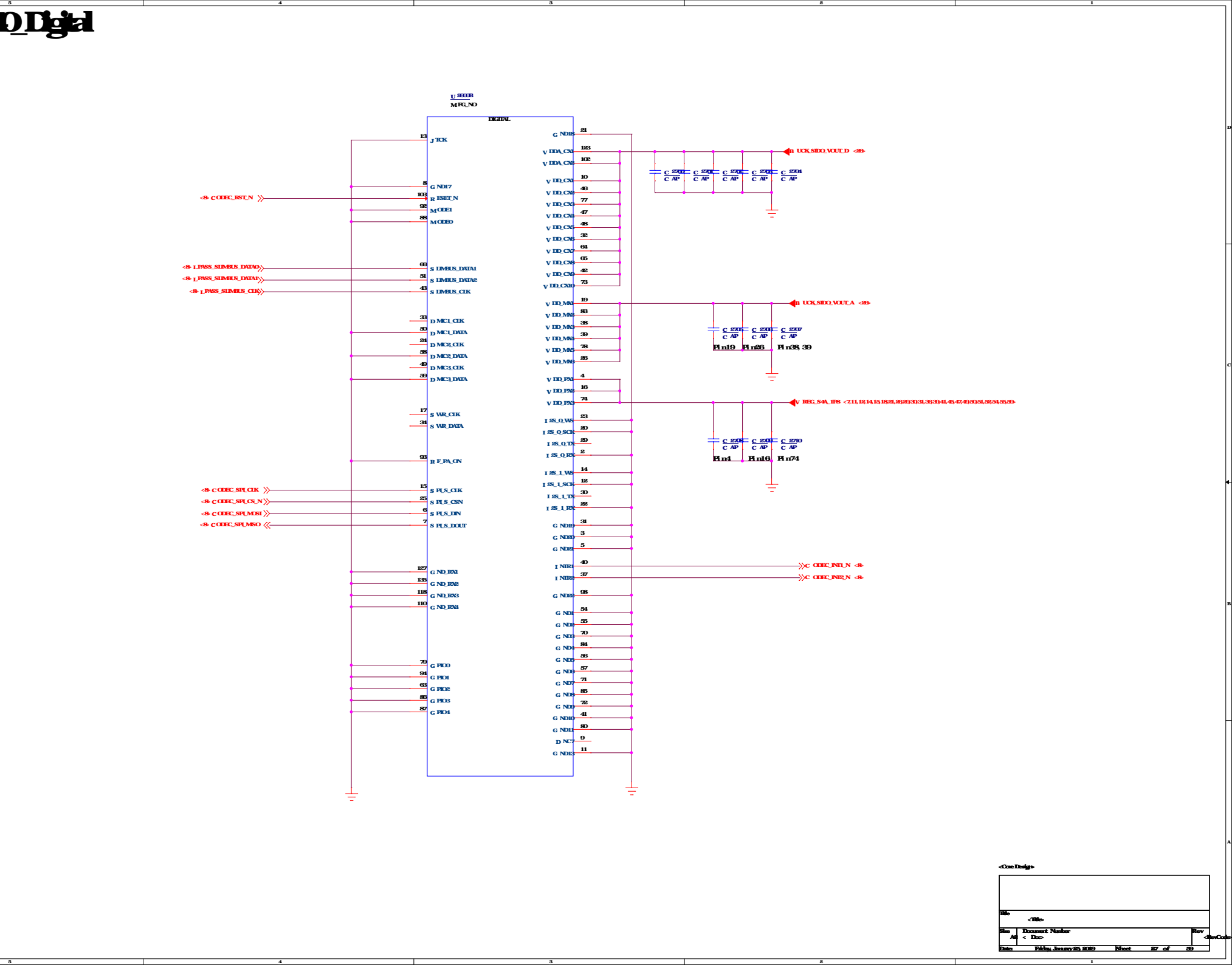
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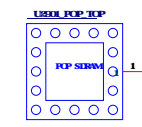
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The diagram illustrates the SPI interface between the STM32F103C8T6 and the ADXL345. The STM32F103C8T6 is connected to the ADXL345 via four pins: CSN (Chip Select), SCK (Serial Clock), MISO (Master In Slave Out), and MOSI (Master Out Slave In). The connections are as follows:

- CSN (ADXL345) is connected to PA7 (STM32F103C8T6).
- SCK (ADXL345) is connected to PA6 (STM32F103C8T6).
- MISO (ADXL345) is connected to PA5 (STM32F103C8T6).
- MOSI (ADXL345) is connected to PA4 (STM32F103C8T6).

The ADXL345 also has a VCC pin connected to a 3.3V supply and a GND pin connected to ground.

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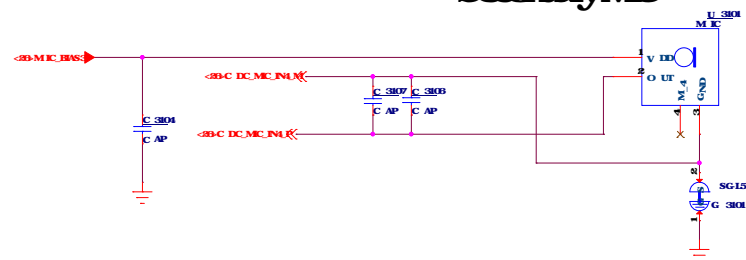
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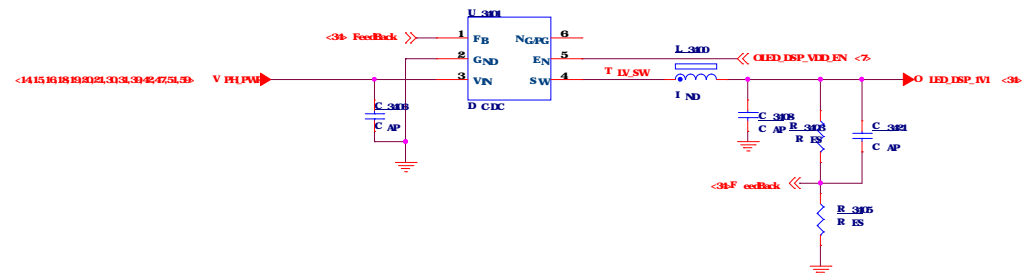
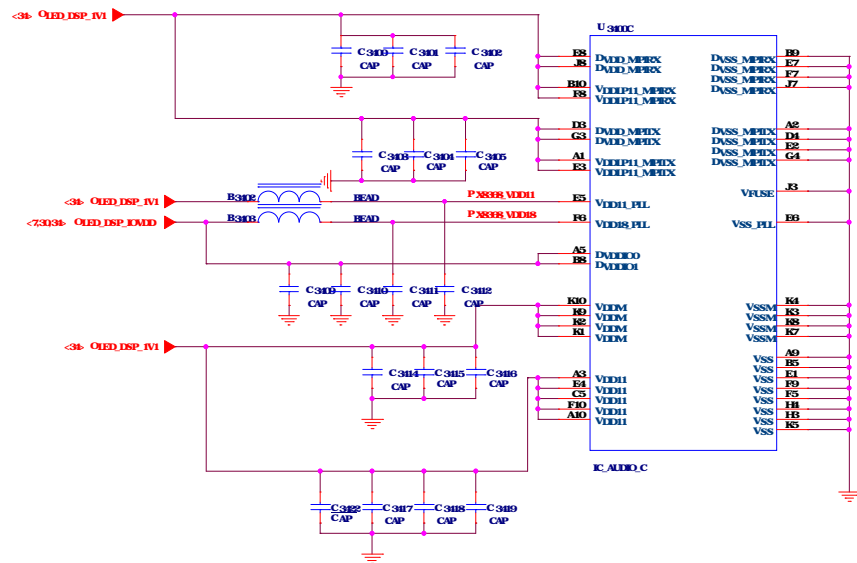
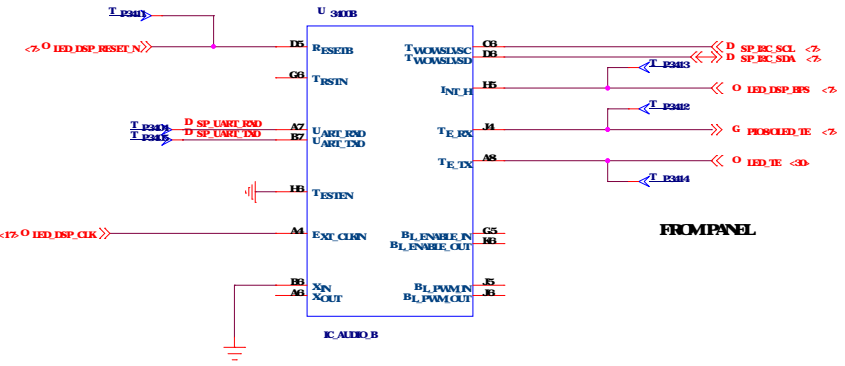
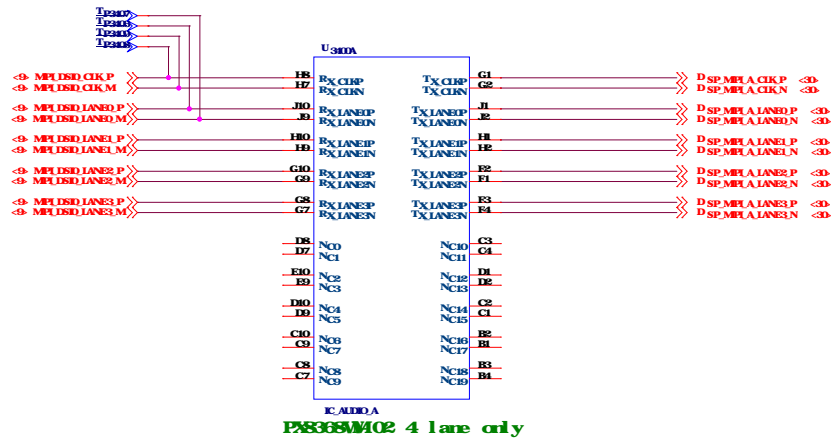
RESERVED
NFC is placed on subboard

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33 OLED Interface

OLED BOOST is placed on subboard, and oled interface is seperated to
mai nboard and subboard



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35 Min Sae Canza Flash

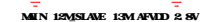
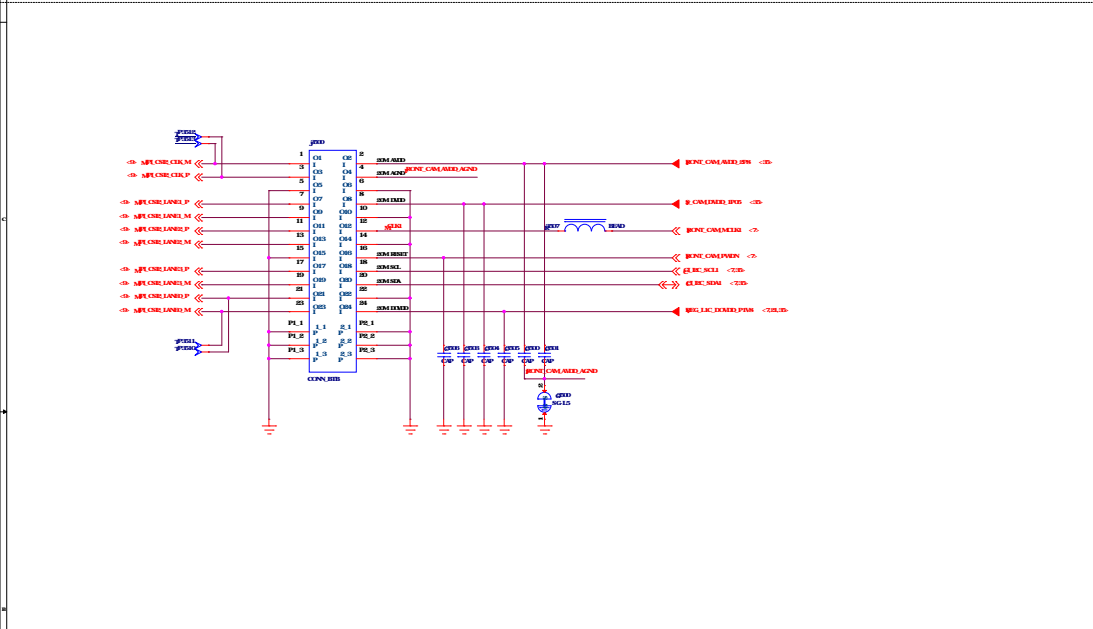
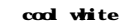


Figure 1: Camera Power ICs. The figure displays six circuit diagrams for different camera power ICs, arranged in a 3x2 grid. Each diagram shows the internal structure of the IC with pins for VDD, GND, and various control signals. The diagrams are labeled as follows:

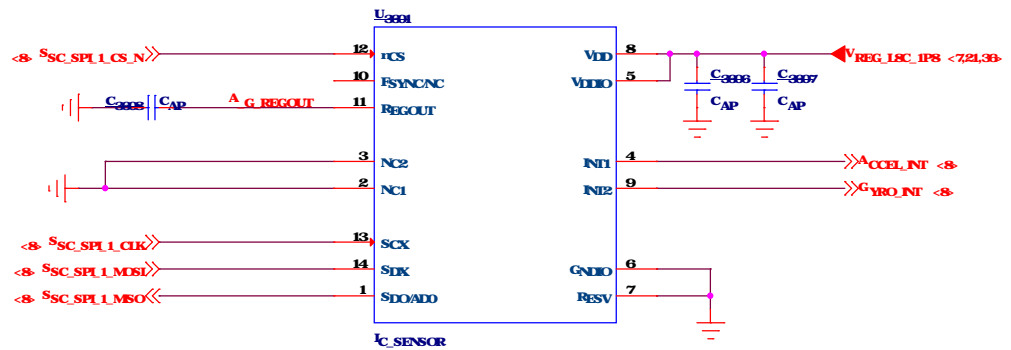
- FRONT 2M/AMD 2.8V**: Shows a circuit with a 2.8V supply and a 100nF capacitor.
- FRONT 2M/SLAVE 13M/AMD 1.05V**: Shows a circuit with a 1.05V supply and a 100nF capacitor.
- SLAVE 13M/AMD 2.8V**: Shows a circuit with a 2.8V supply and a 100nF capacitor.
- MIN 4M/AMD 1.05V**: Shows a circuit with a 1.05V supply and a 100nF capacitor.
- MIN 4M/AMD 2.8V**: Shows a circuit with a 2.8V supply and a 100nF capacitor.
- MIN 12M/SLAVE 13M/AMD 2.8V**: Shows a circuit with a 2.8V supply and a 100nF capacitor.



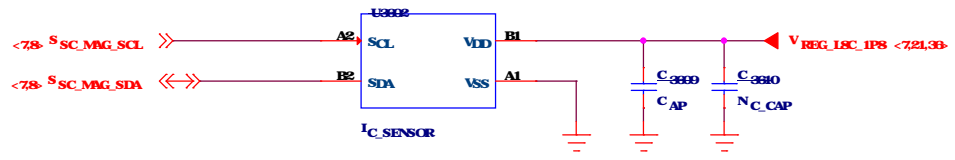
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10K

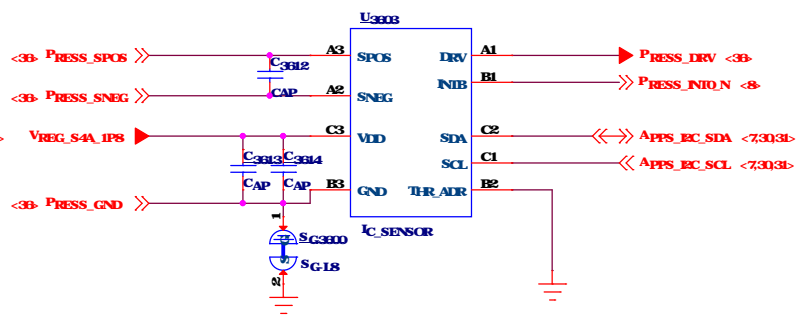
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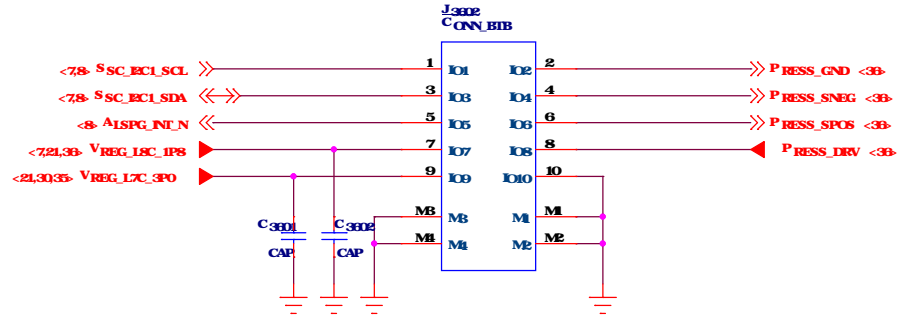
A+G



E COMPASS



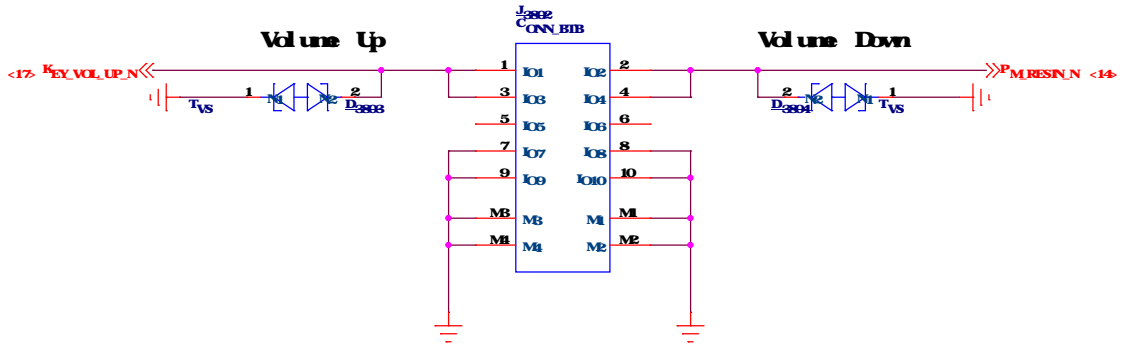
press sensor controller



press sensor

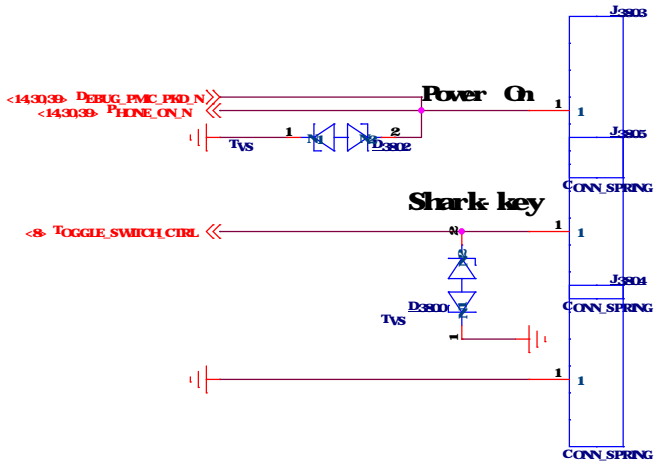
ALS+PROX

RESERVED
SIMis placed on subboard



KEYPADS

RGB Q 7 1. 1mm



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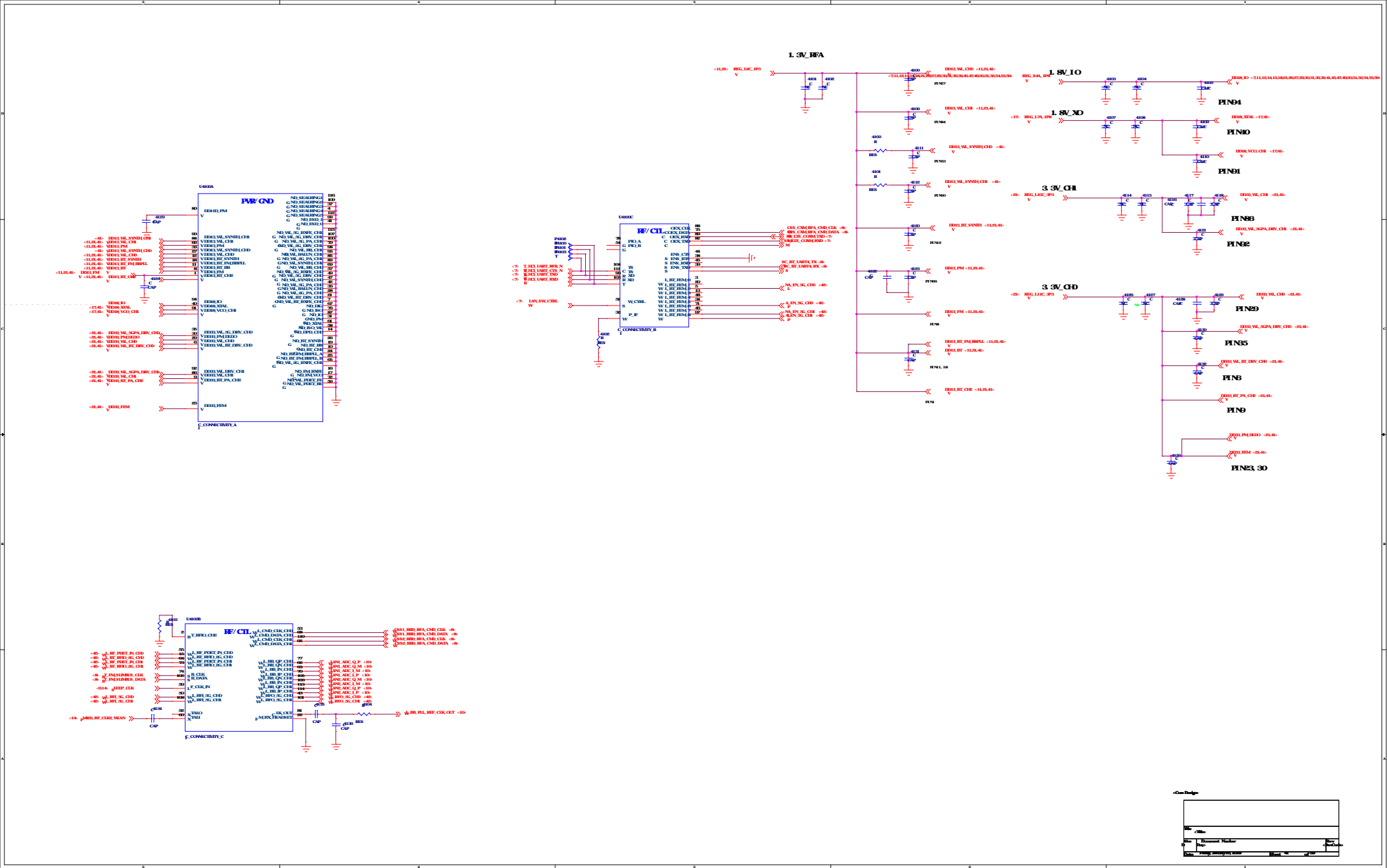
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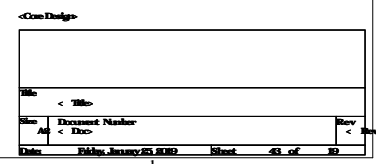
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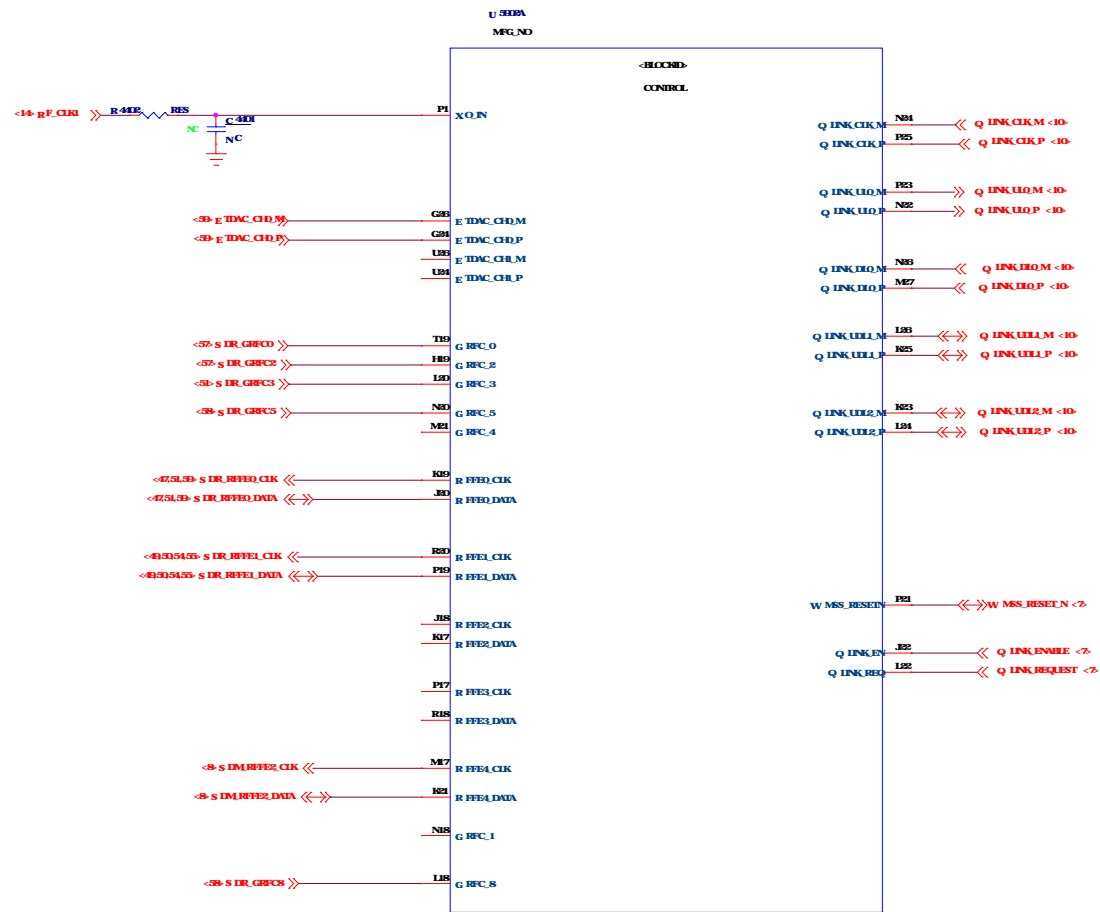
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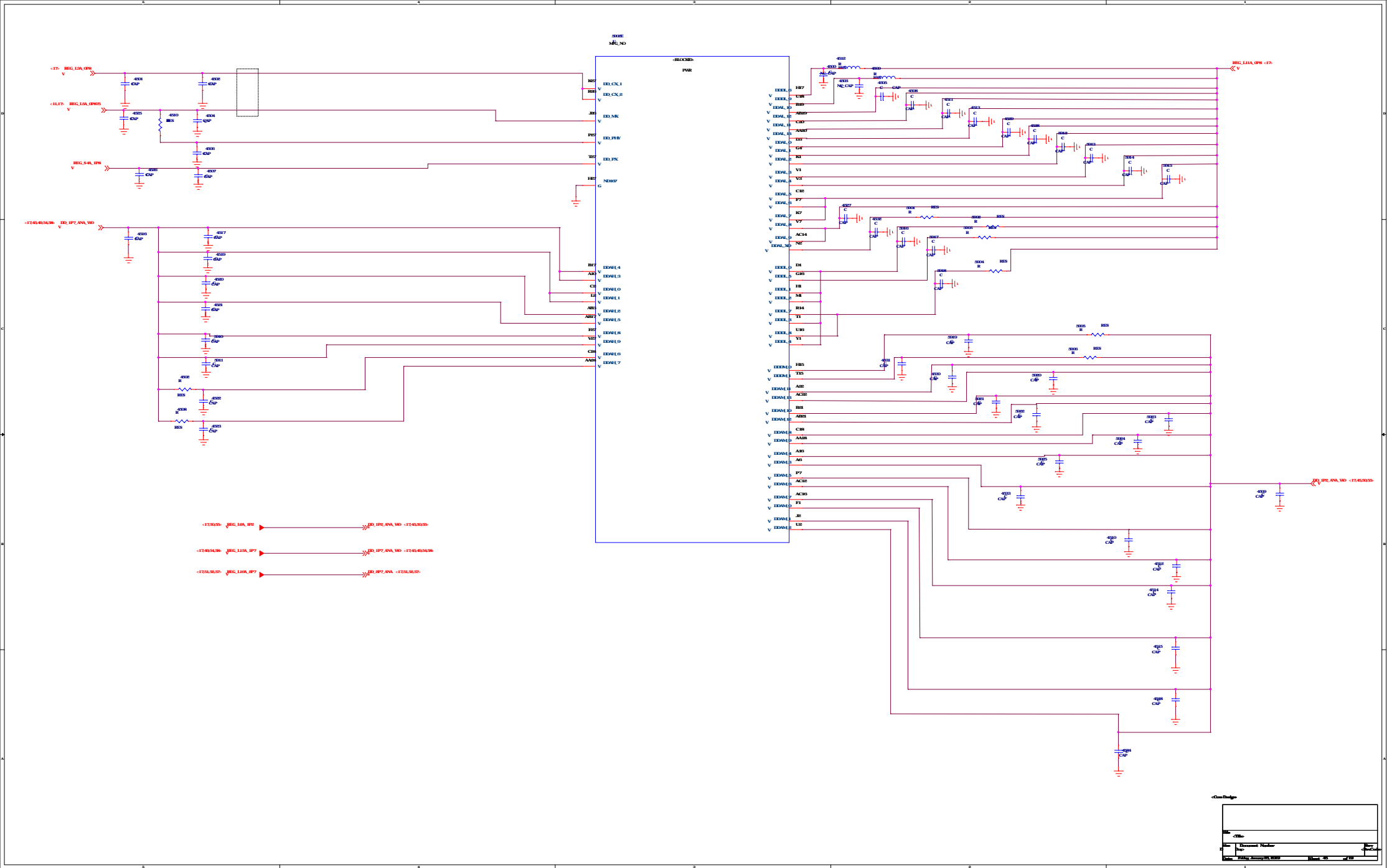
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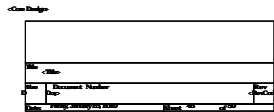
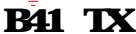
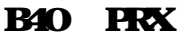


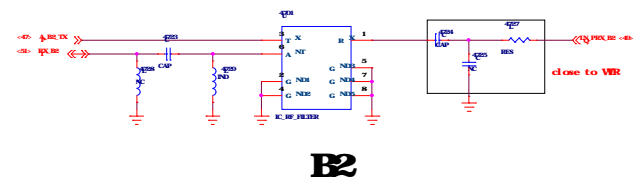
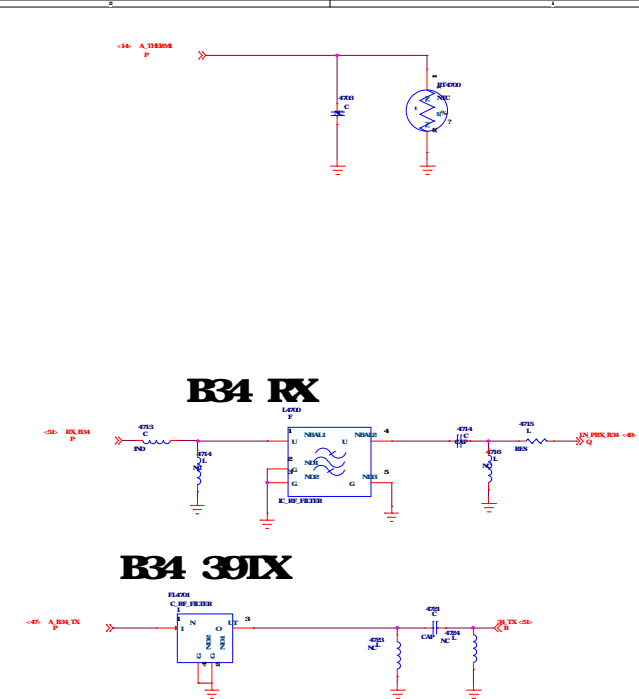


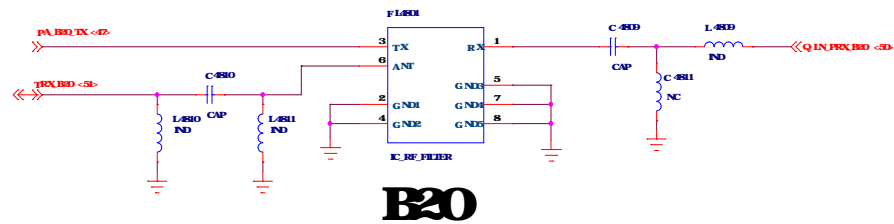
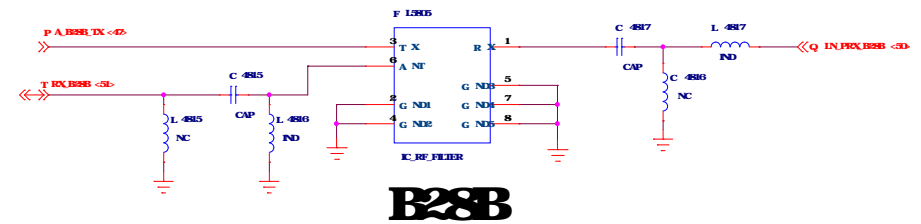
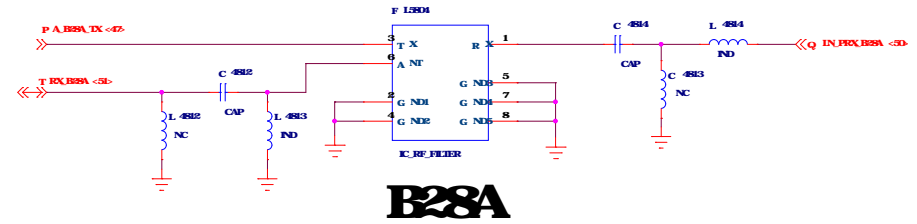
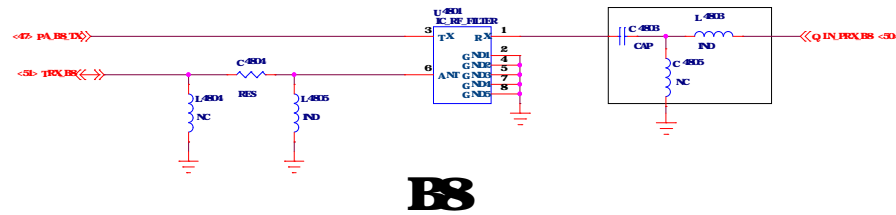
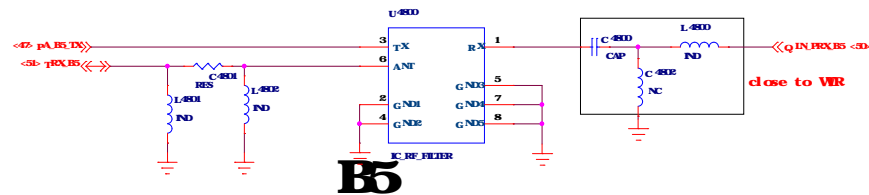
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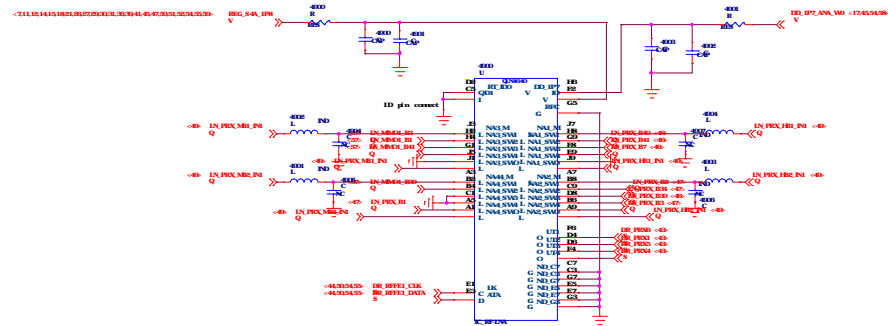


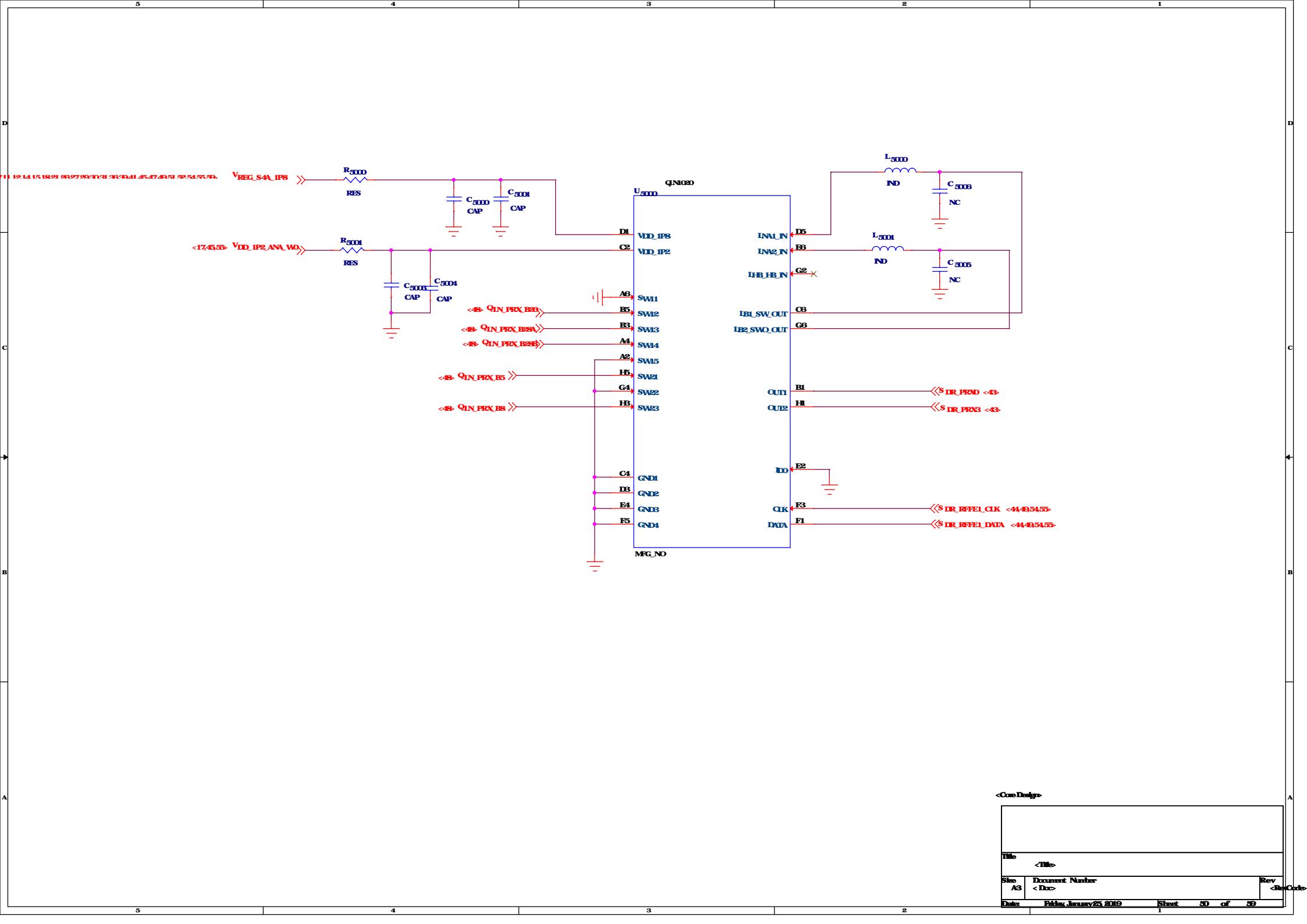


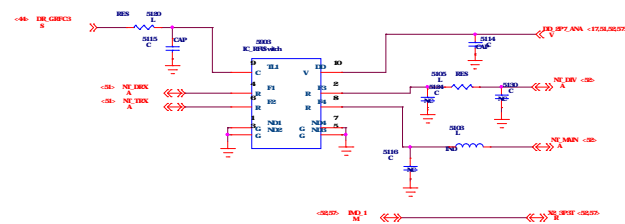
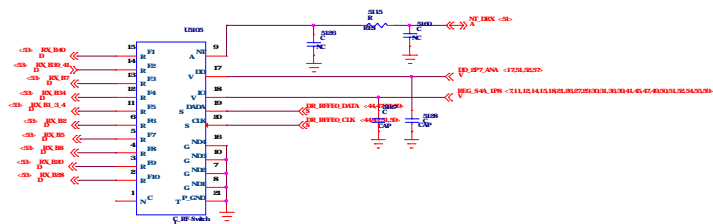
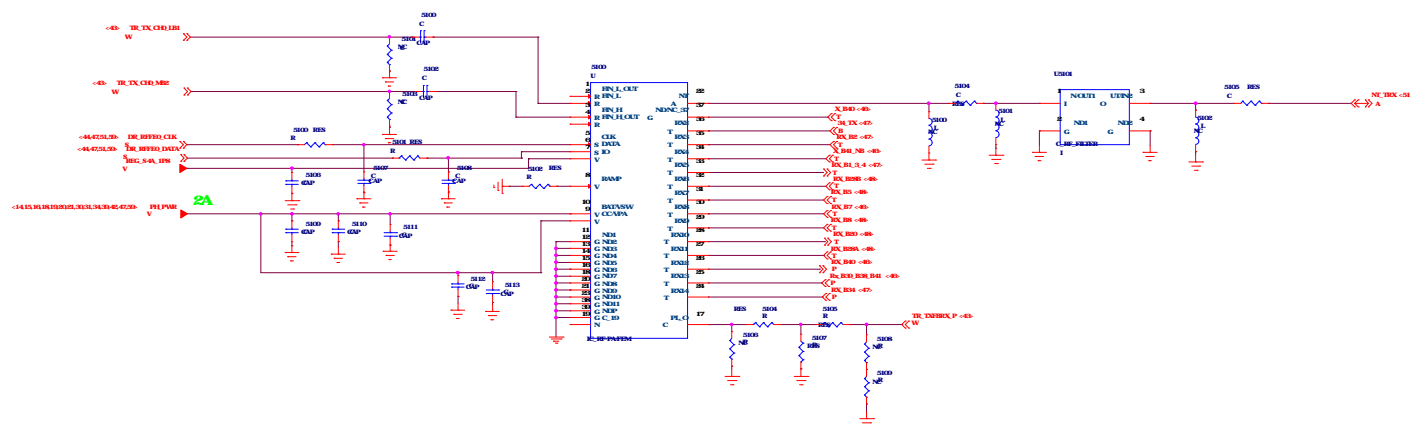


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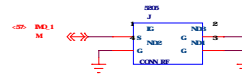
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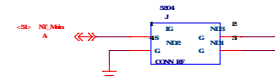


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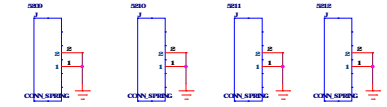
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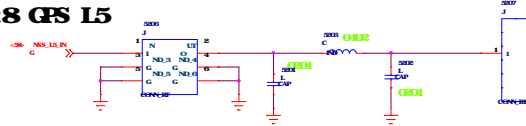
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Cable Fix

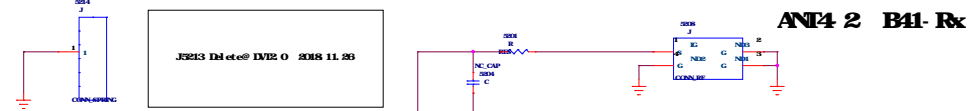


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Art4 1 IC

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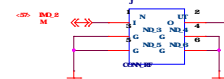


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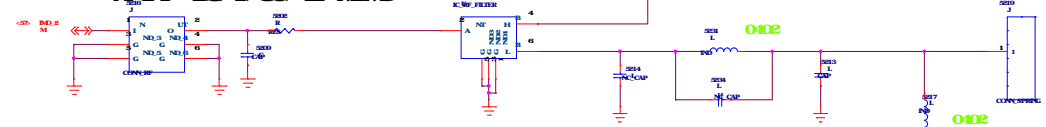
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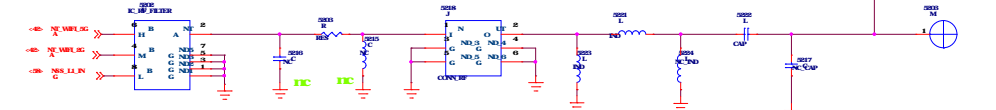
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Art2 Diversity IMB



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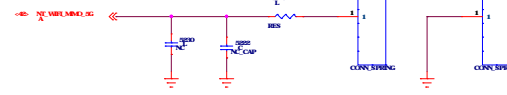


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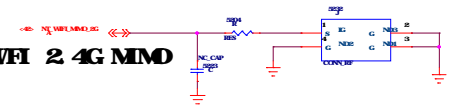
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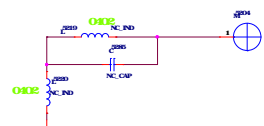
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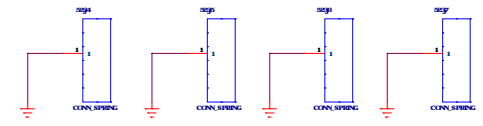
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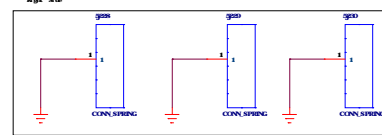
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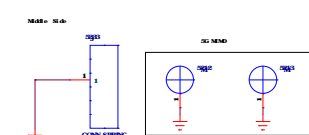
Grounding to Camera Frame



Grounding to A Frame



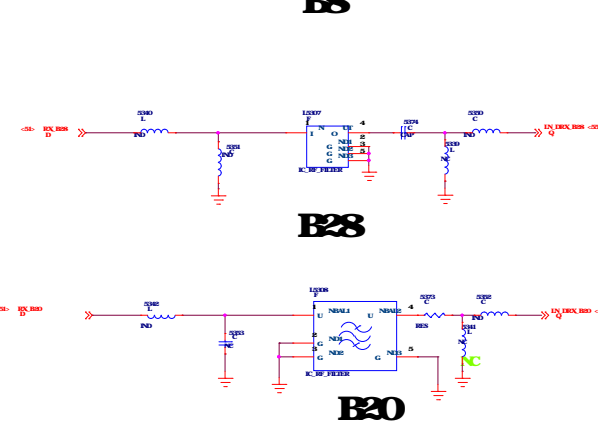
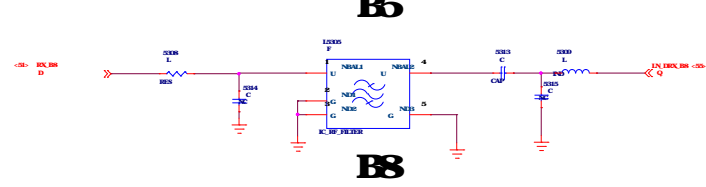
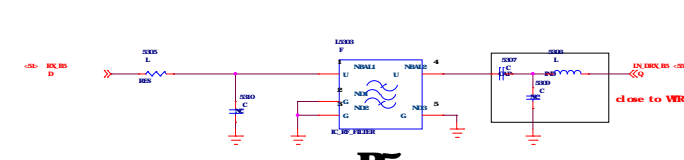
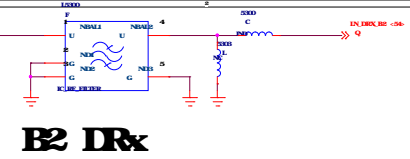
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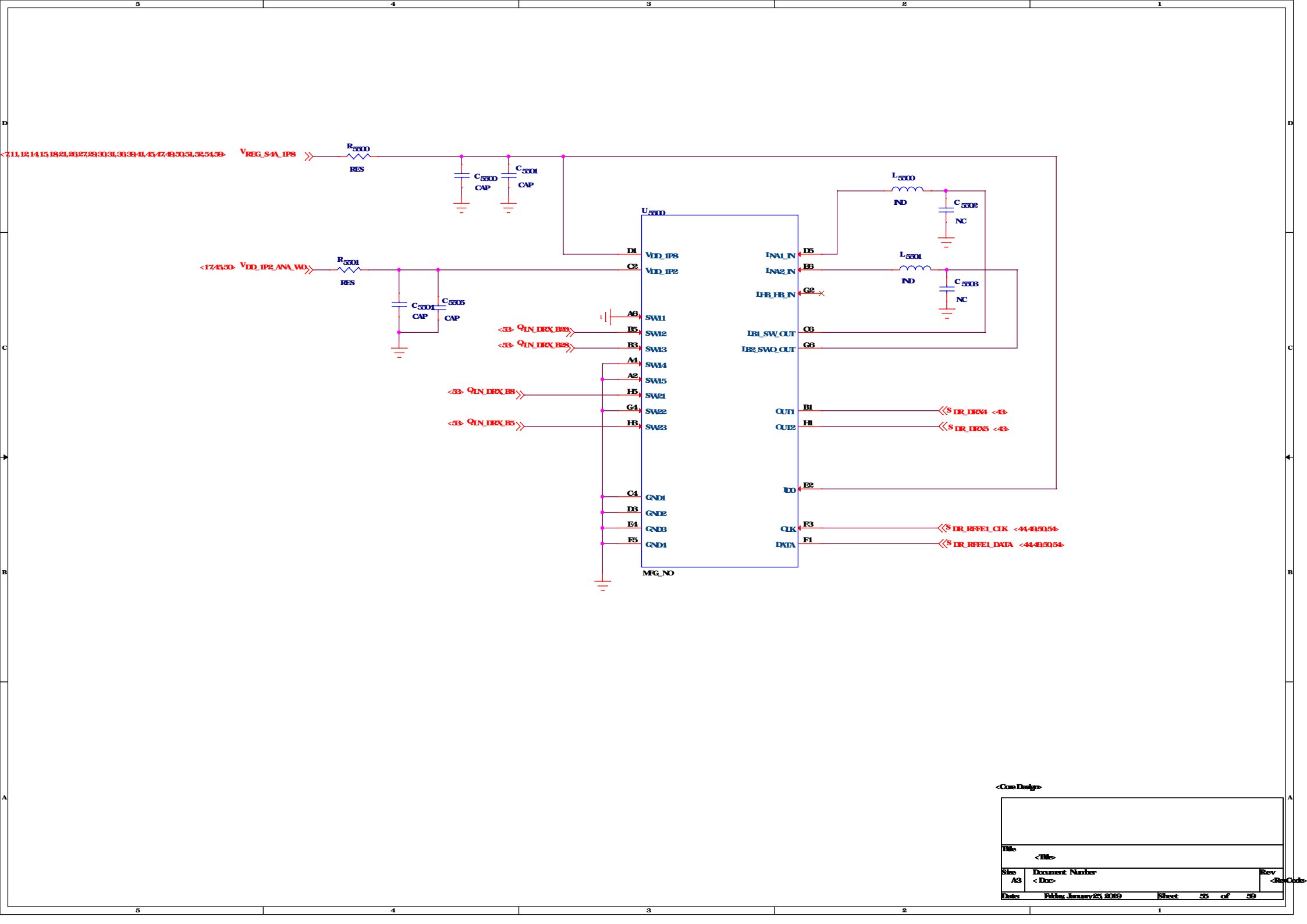


Conducting

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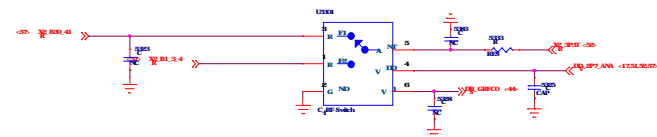
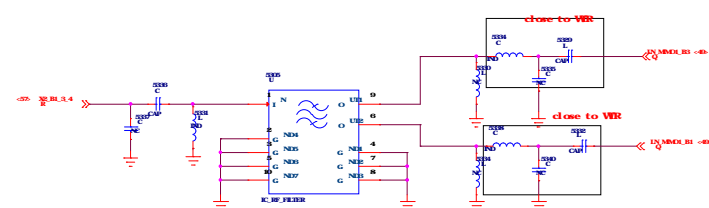
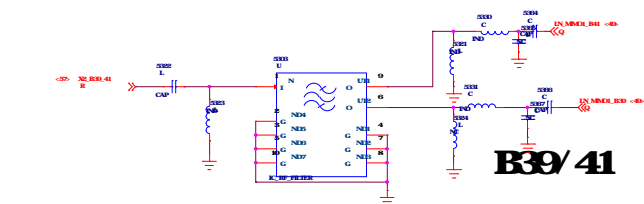
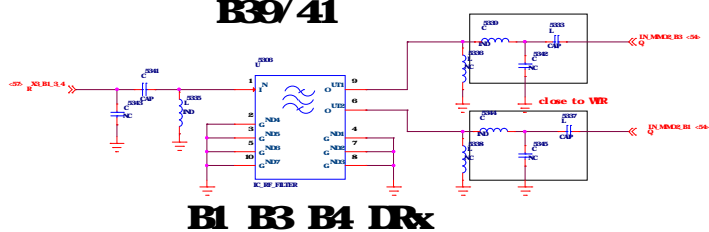
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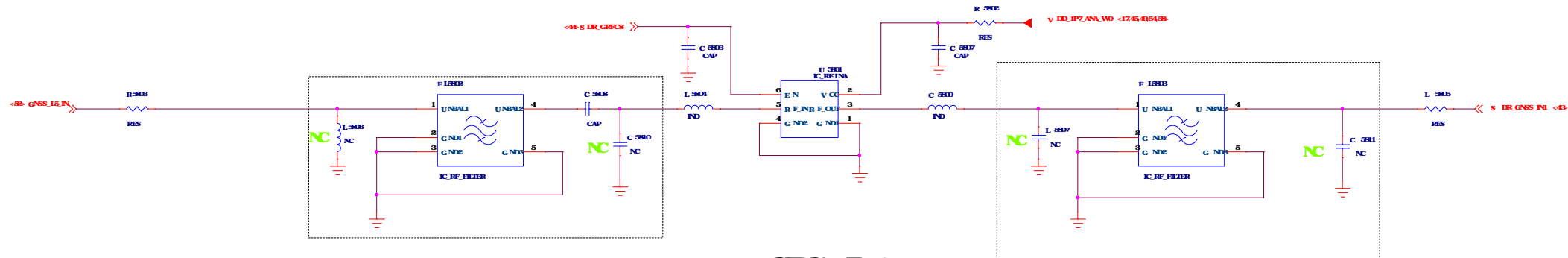
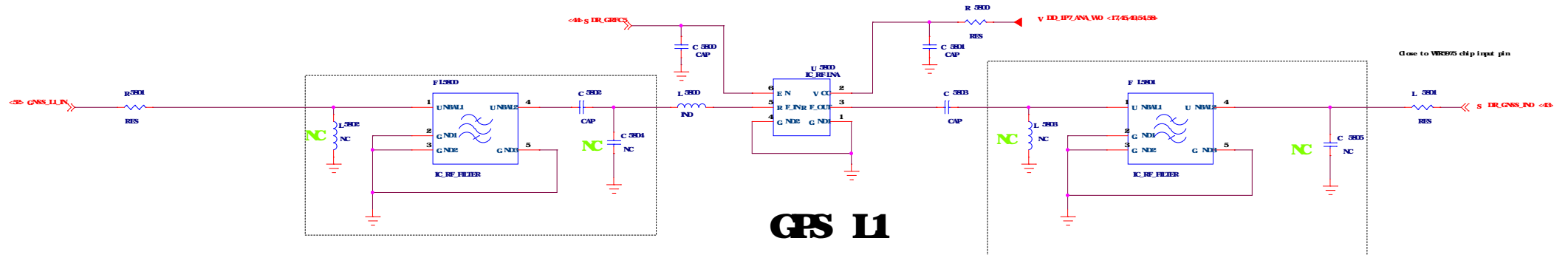






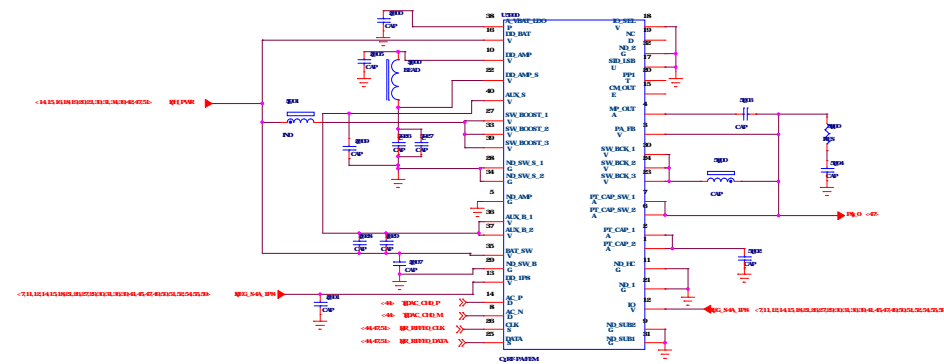
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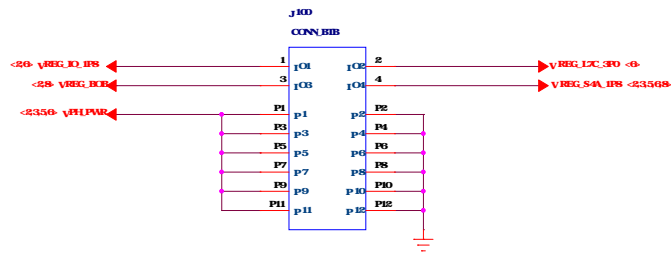




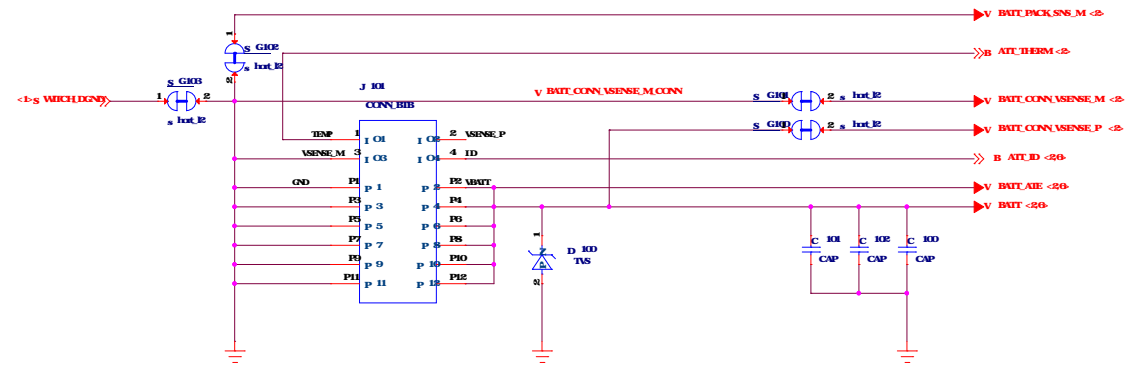
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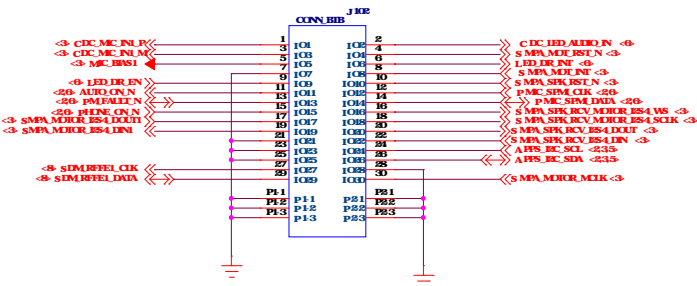




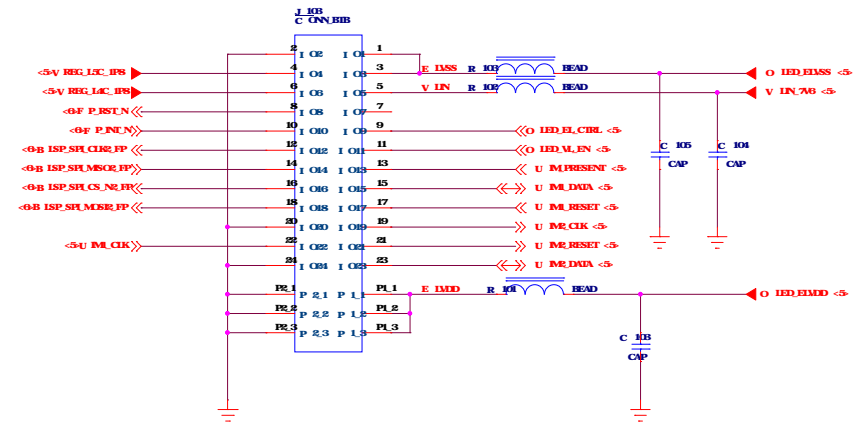
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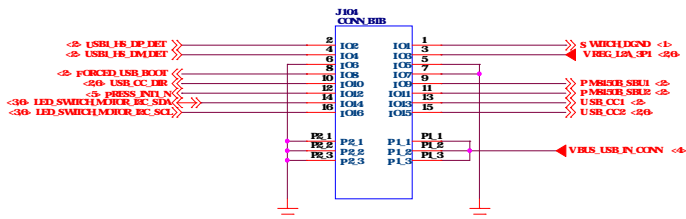
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MIN_TO_SUBBOARD



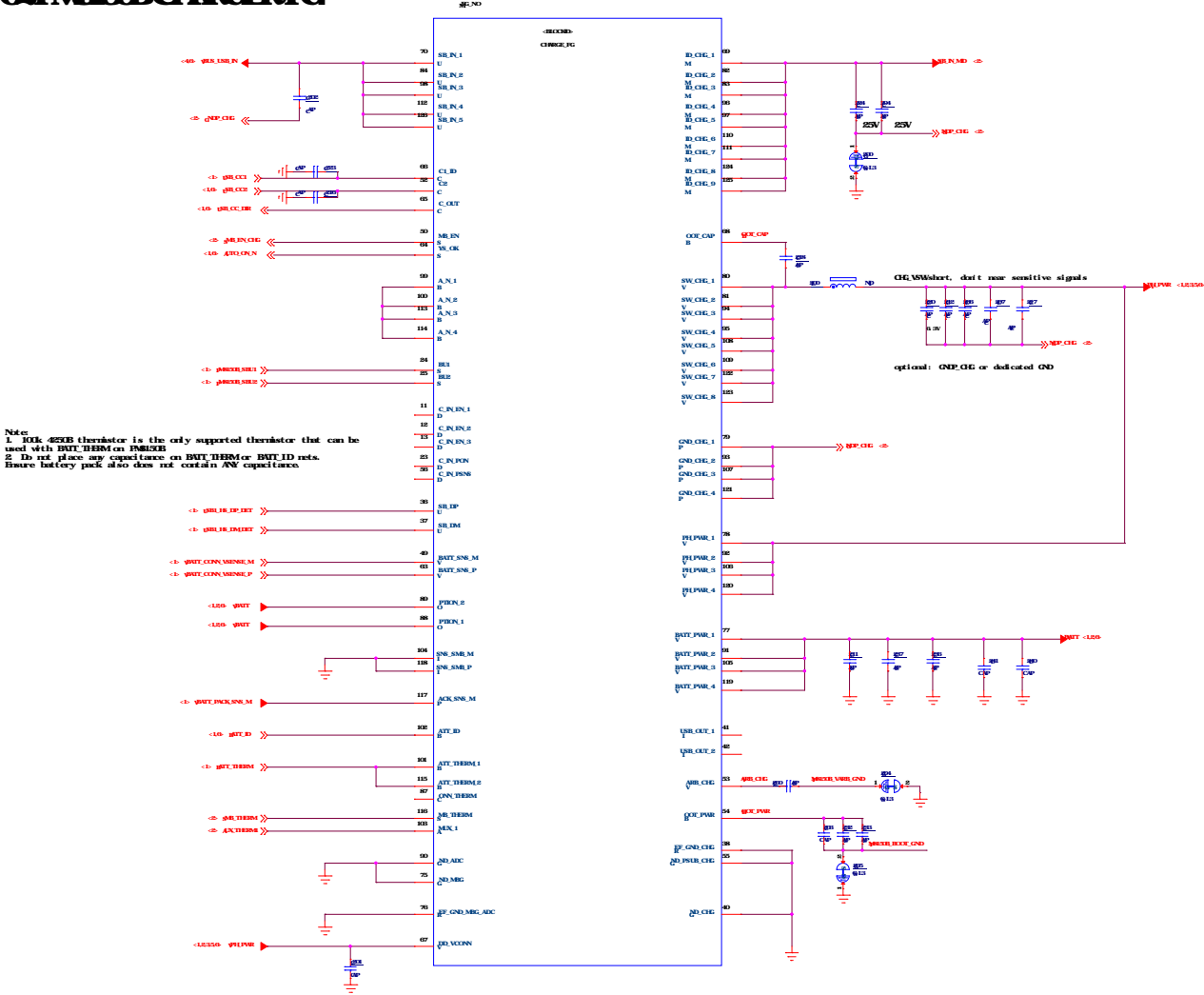
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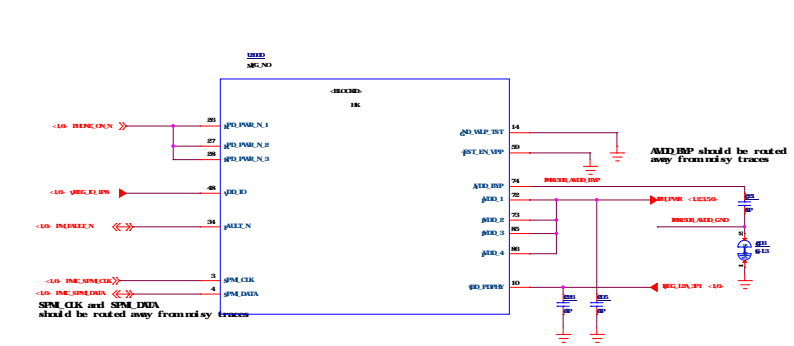
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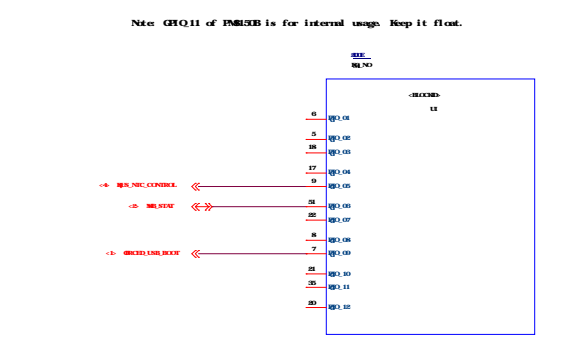
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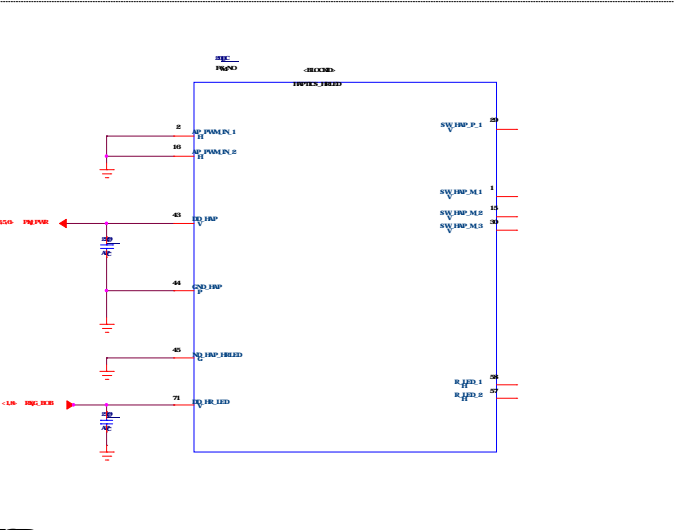
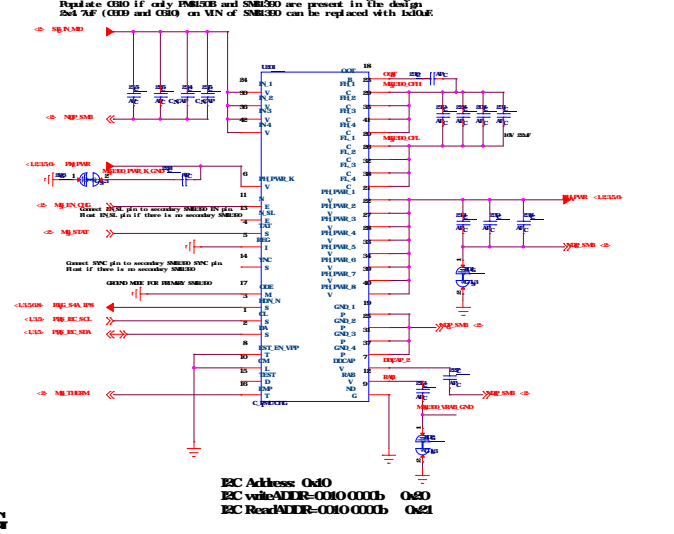
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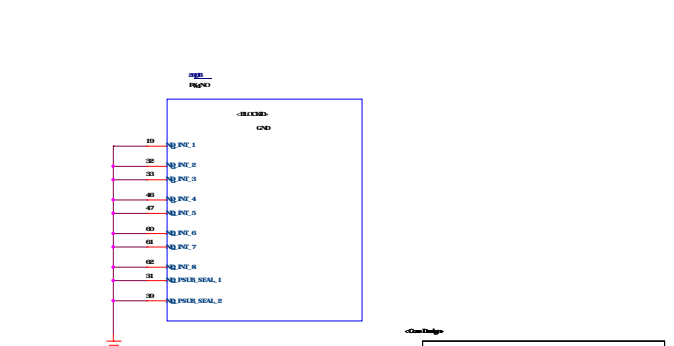
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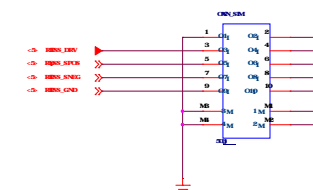
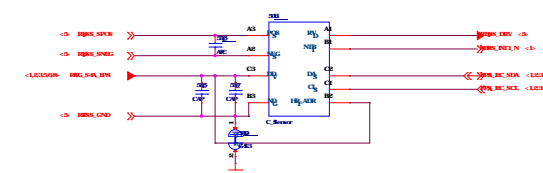
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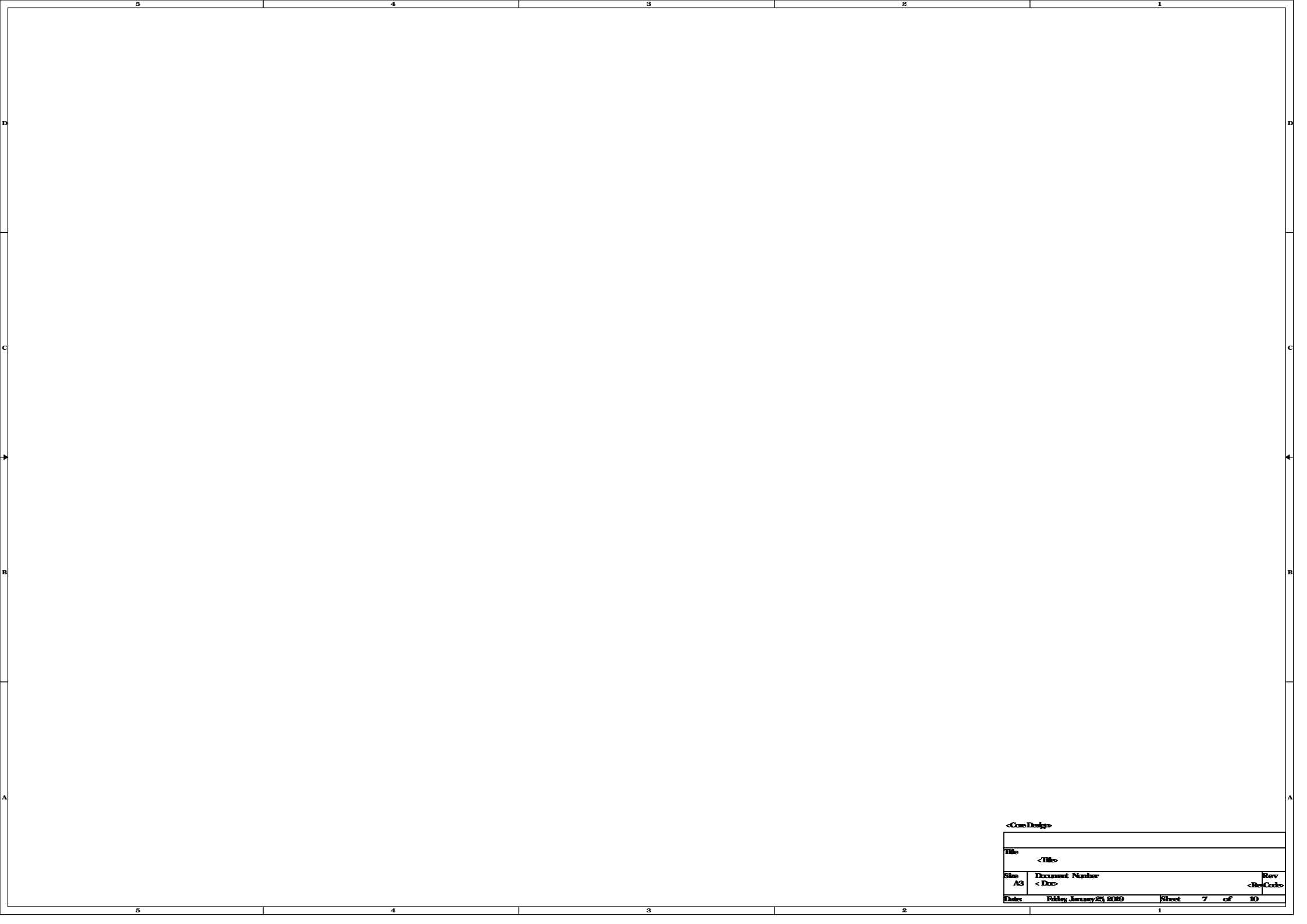
LED BOOST



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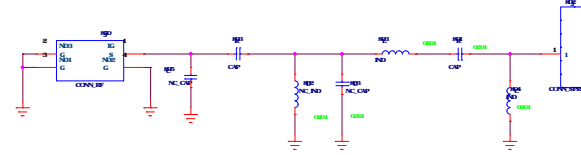
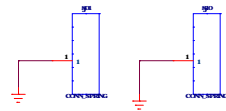


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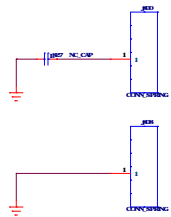
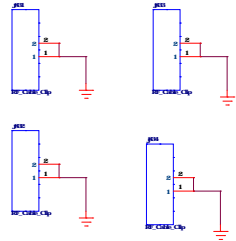
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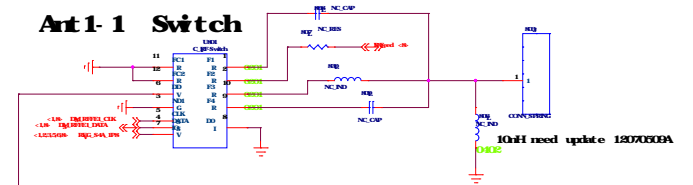
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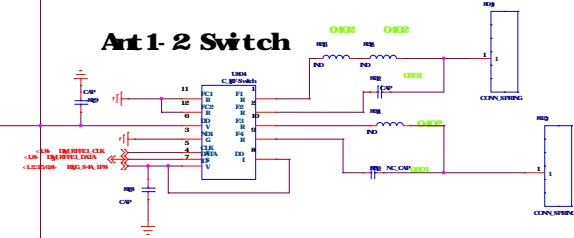
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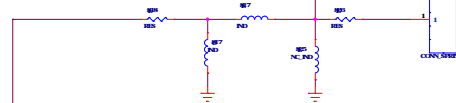
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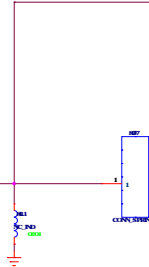
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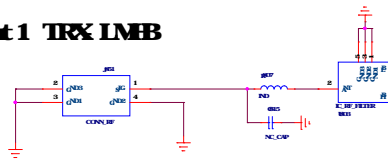
LB 1#



NB 1#

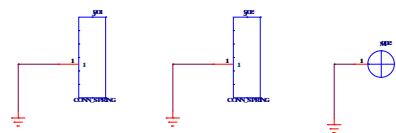


Ant 1 TR& LMB

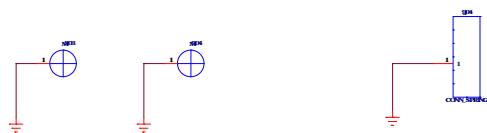


Antenna Grounding

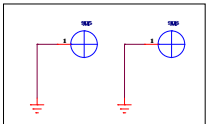
Bottom



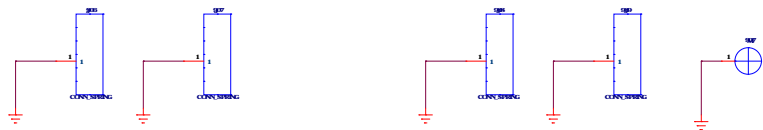
Top



Type C Grounding



Ant 1- LB Switch Top Side



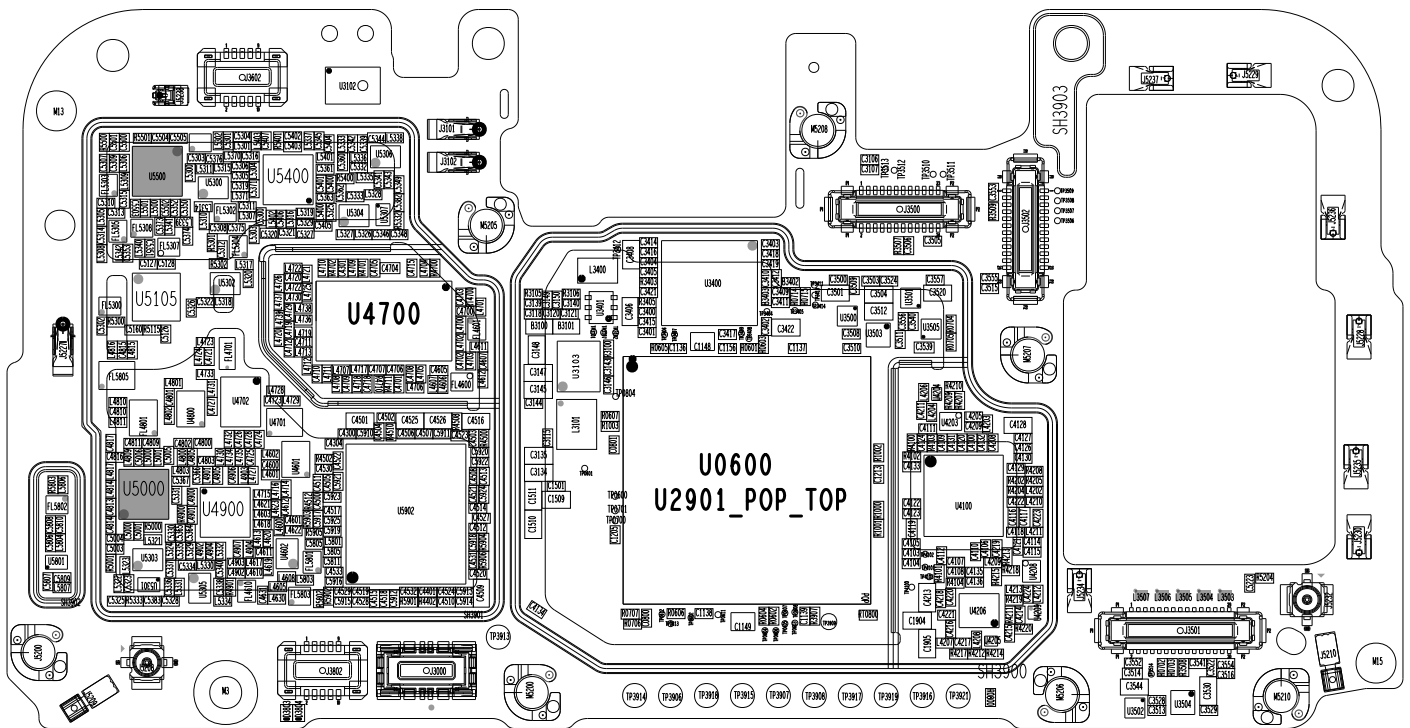
Revision History

REV	DESCRIPTION
V0	First release

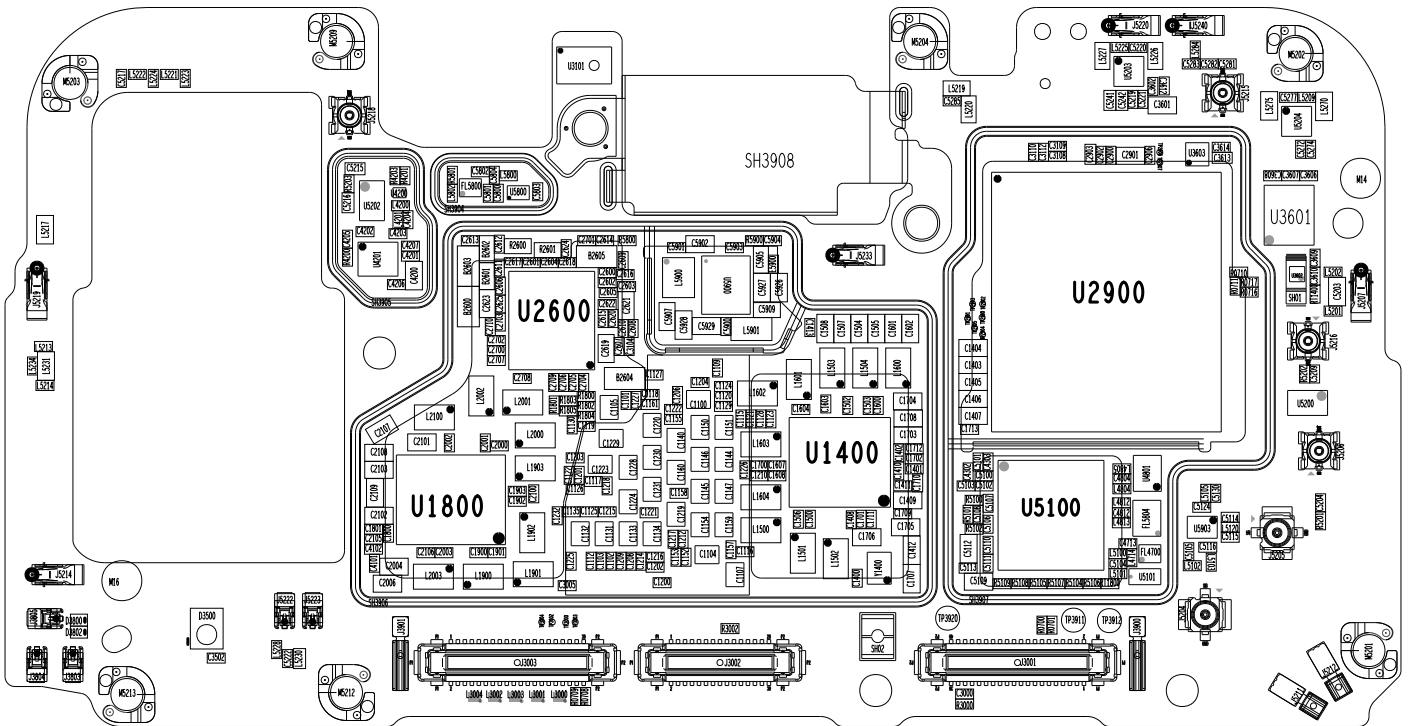
Board Change History

SCHEMATIC MN	HWVERSION	REVISION	DESCRIPTION OF CHANGE

ART FILM - 31_REF_TOP



ART FILM - 32_REF_BOTTOM



ART FILM - 32_REF_BOTTOM